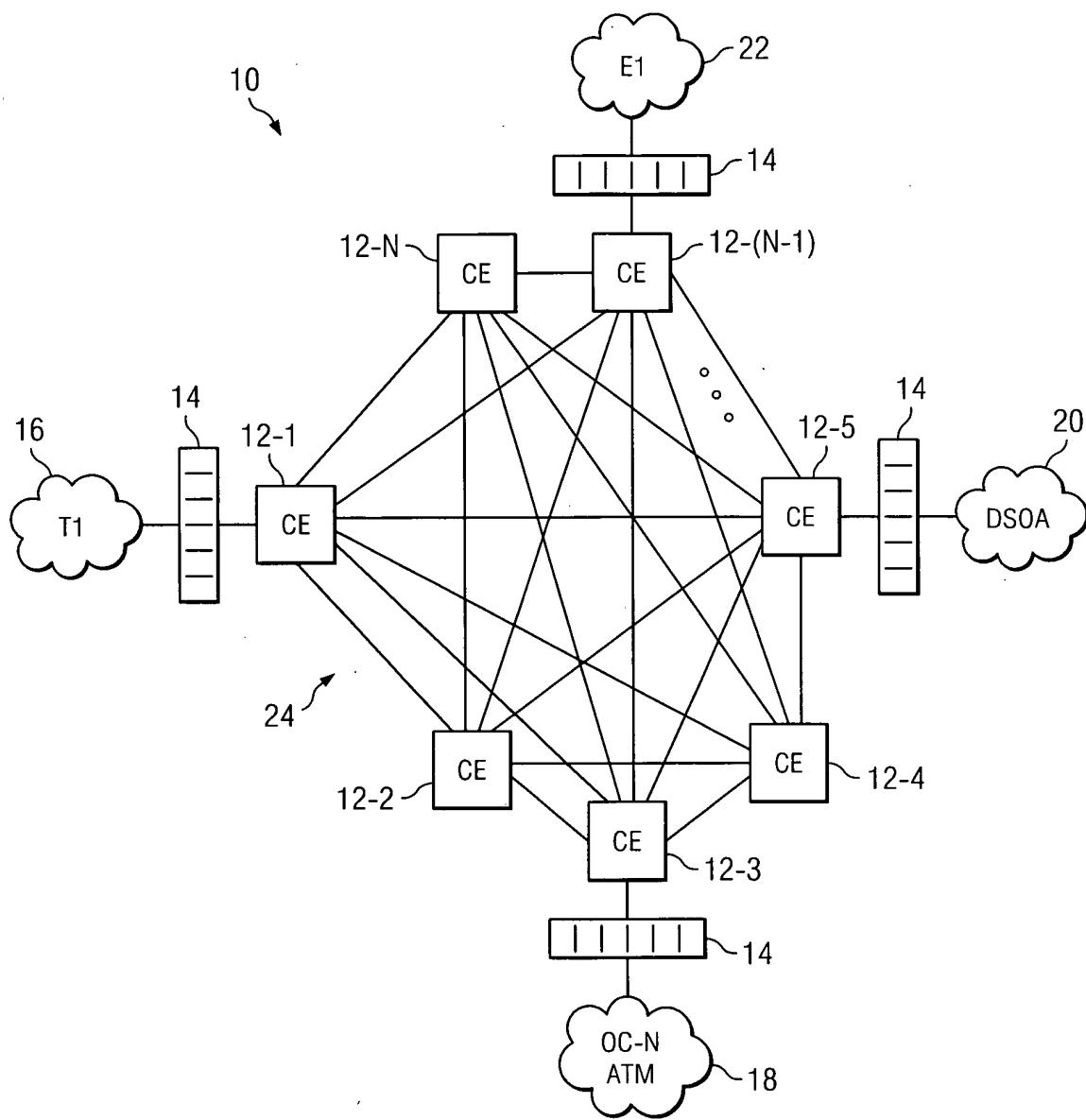


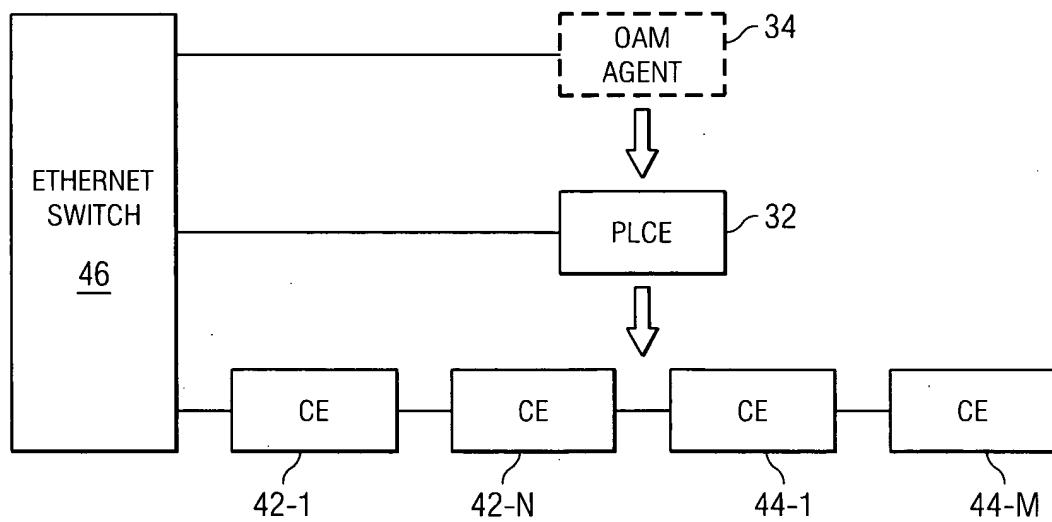
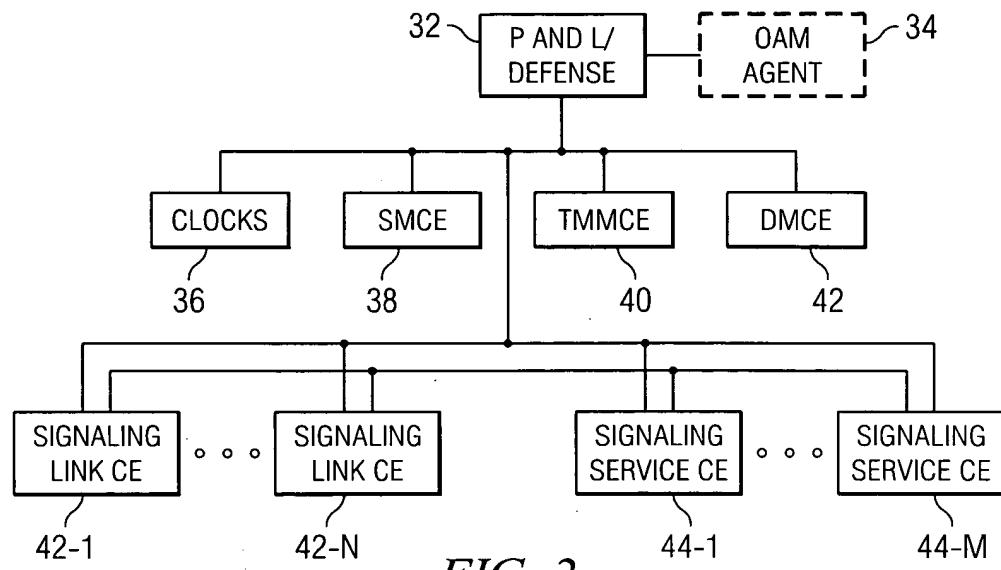
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FIG. 1





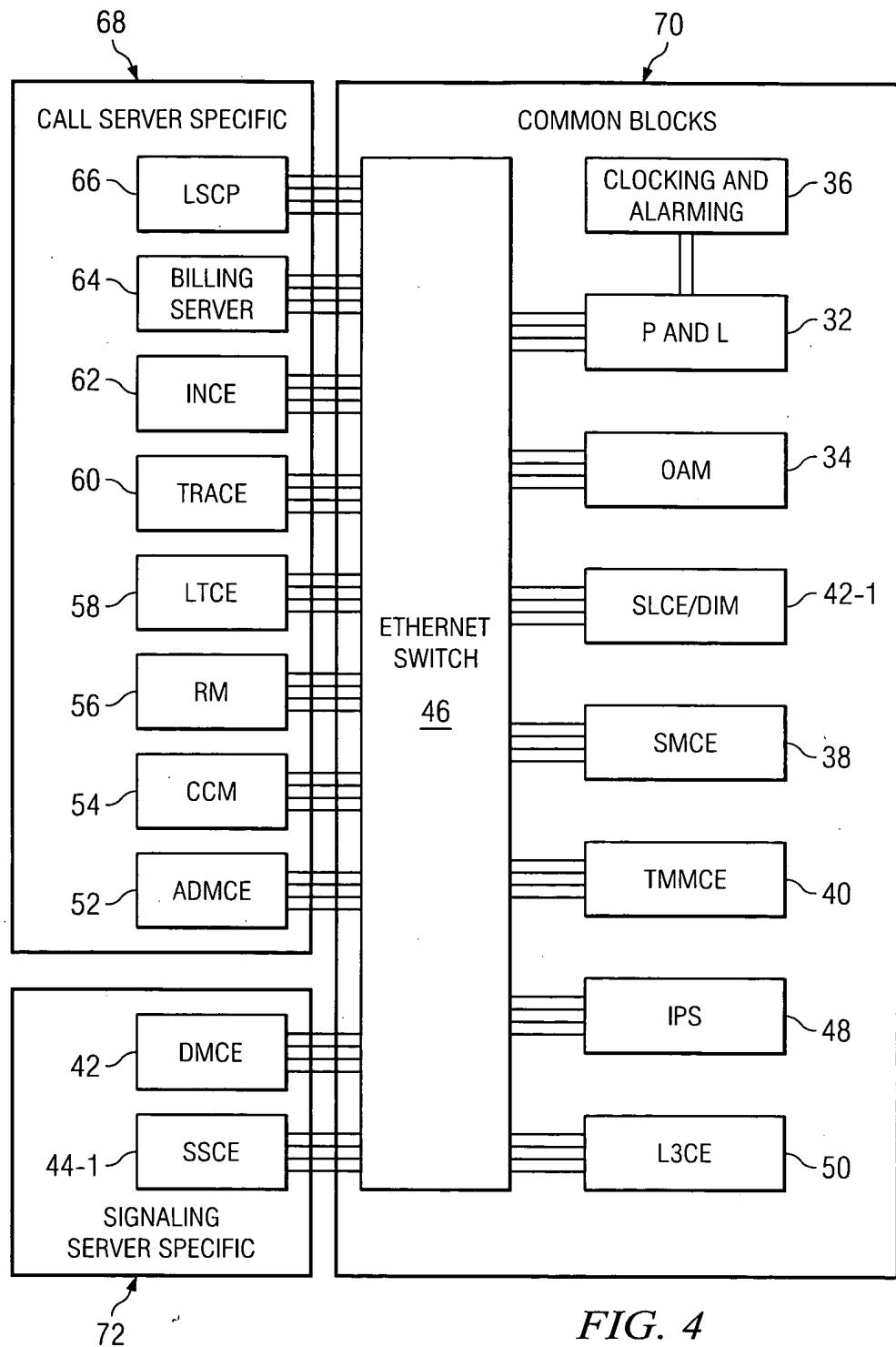


FIG. 4

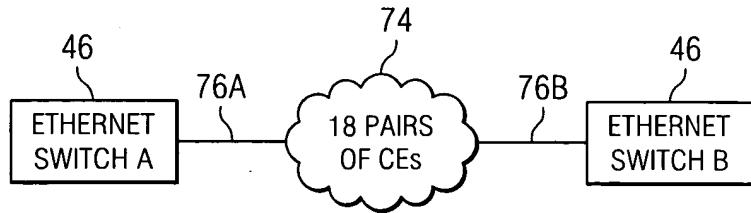


FIG. 5A

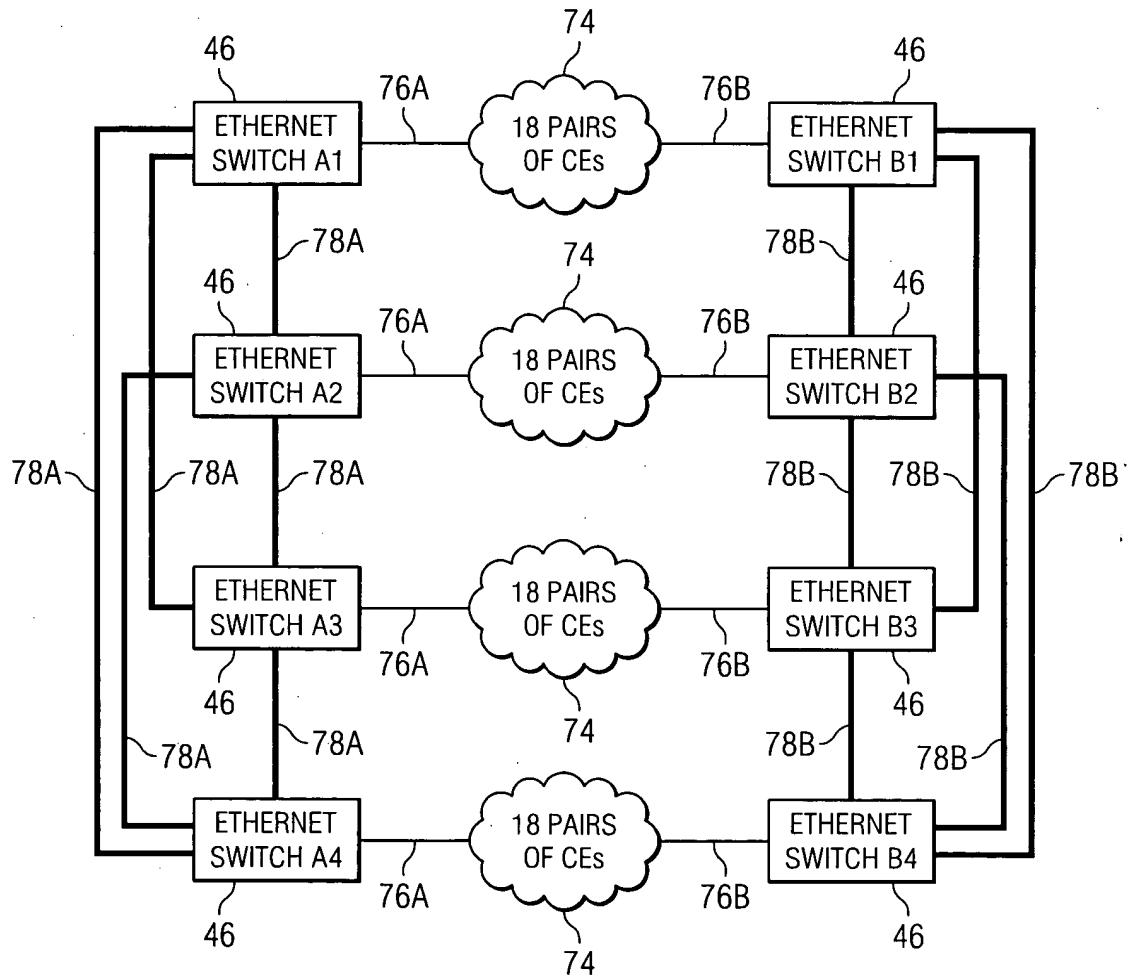


FIG. 5B

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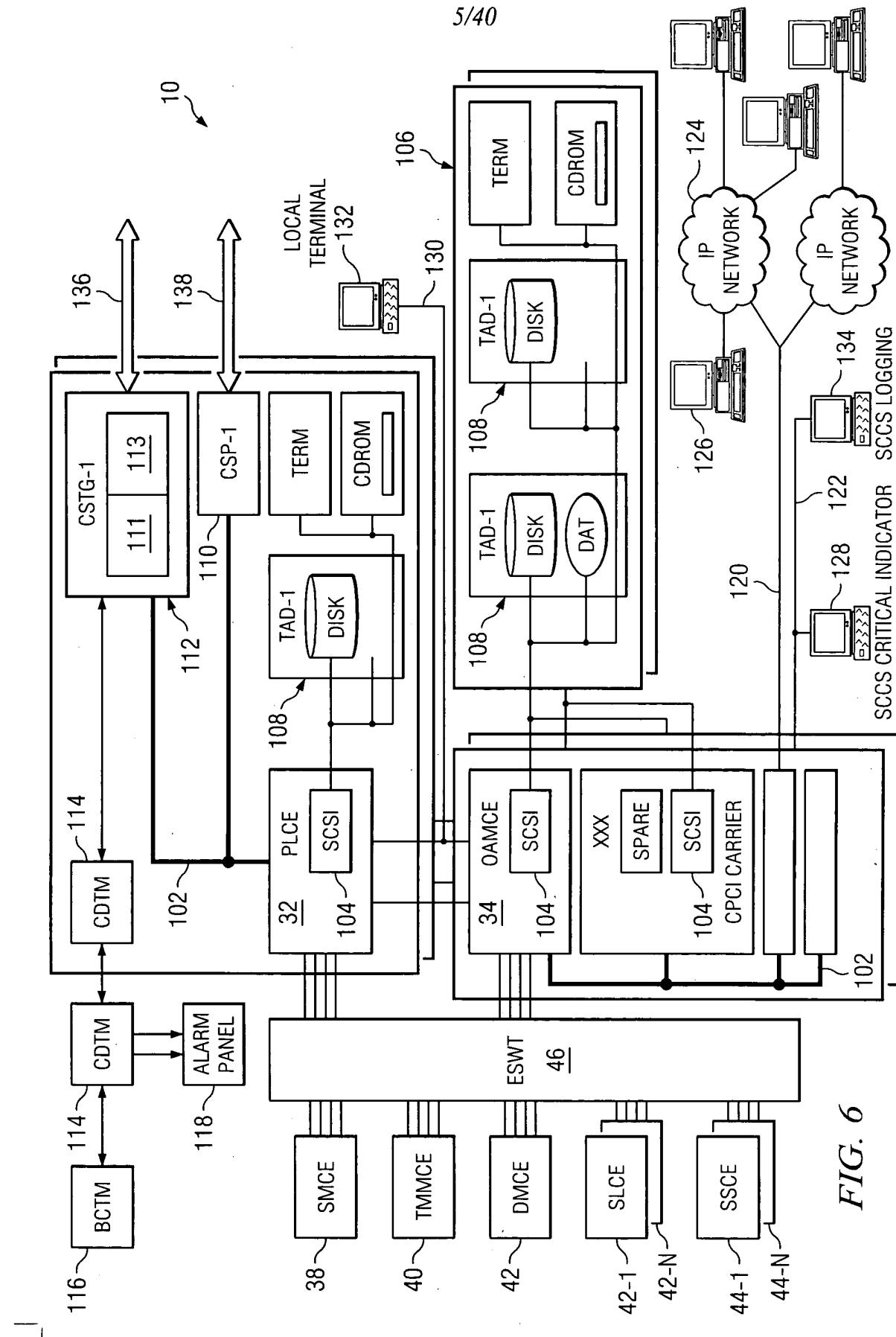


FIG. 6

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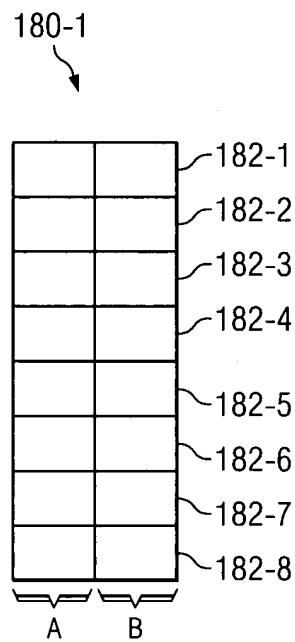


FIG. 7

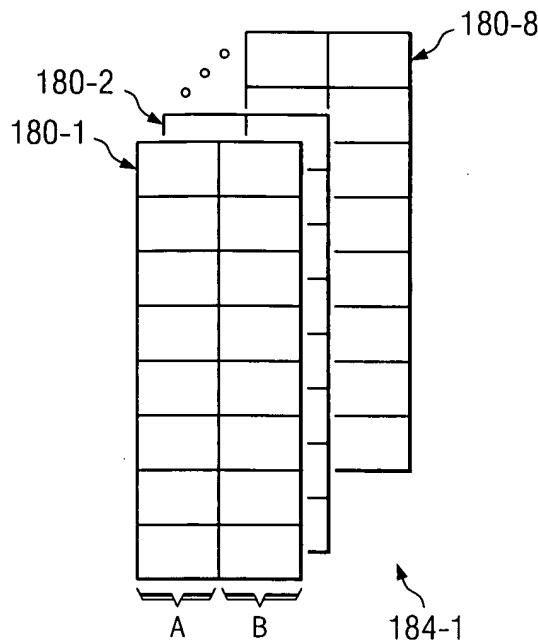


FIG. 8

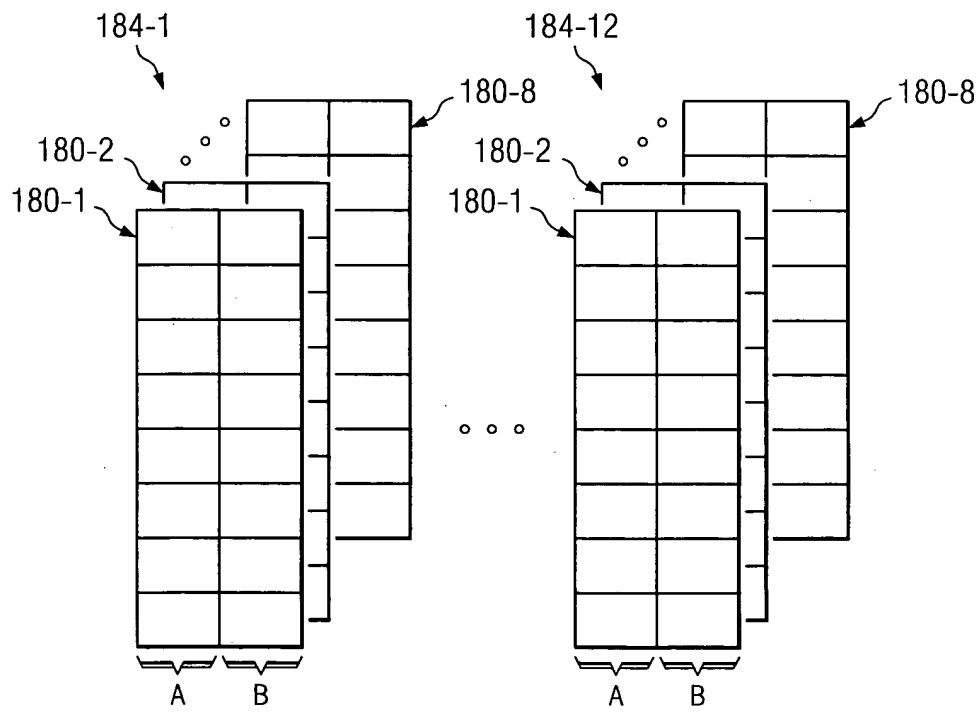


FIG. 9

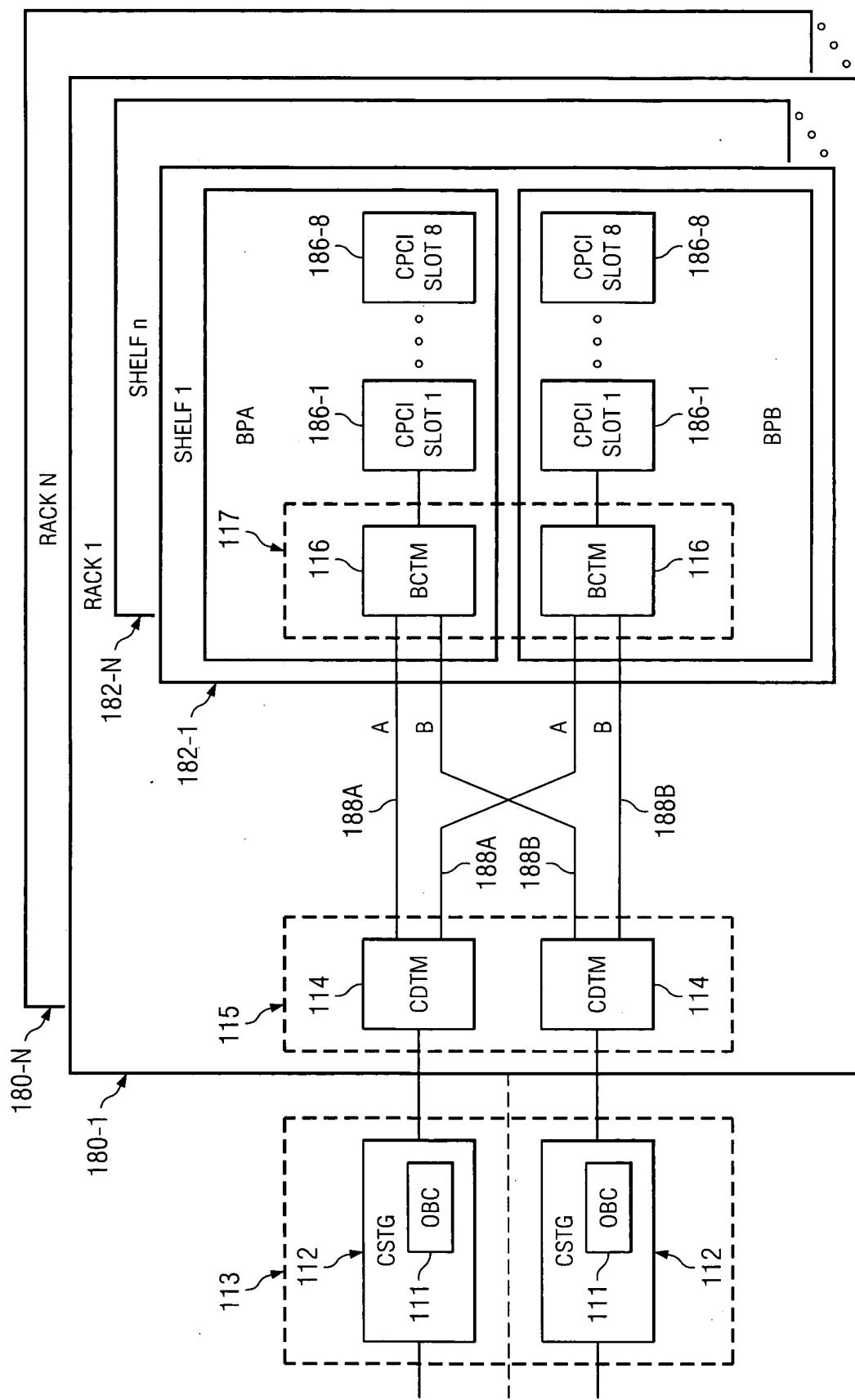


FIG. 10A

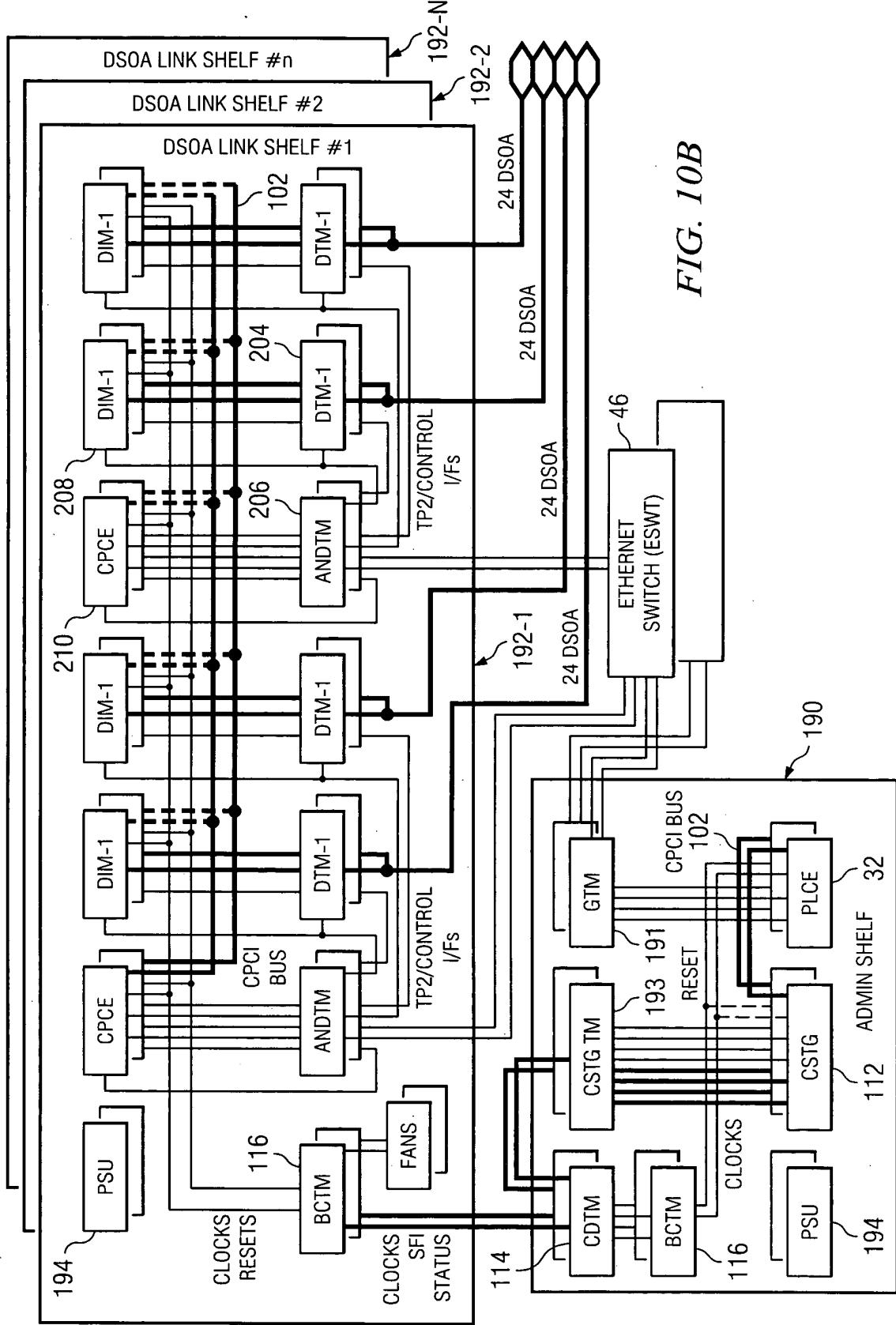
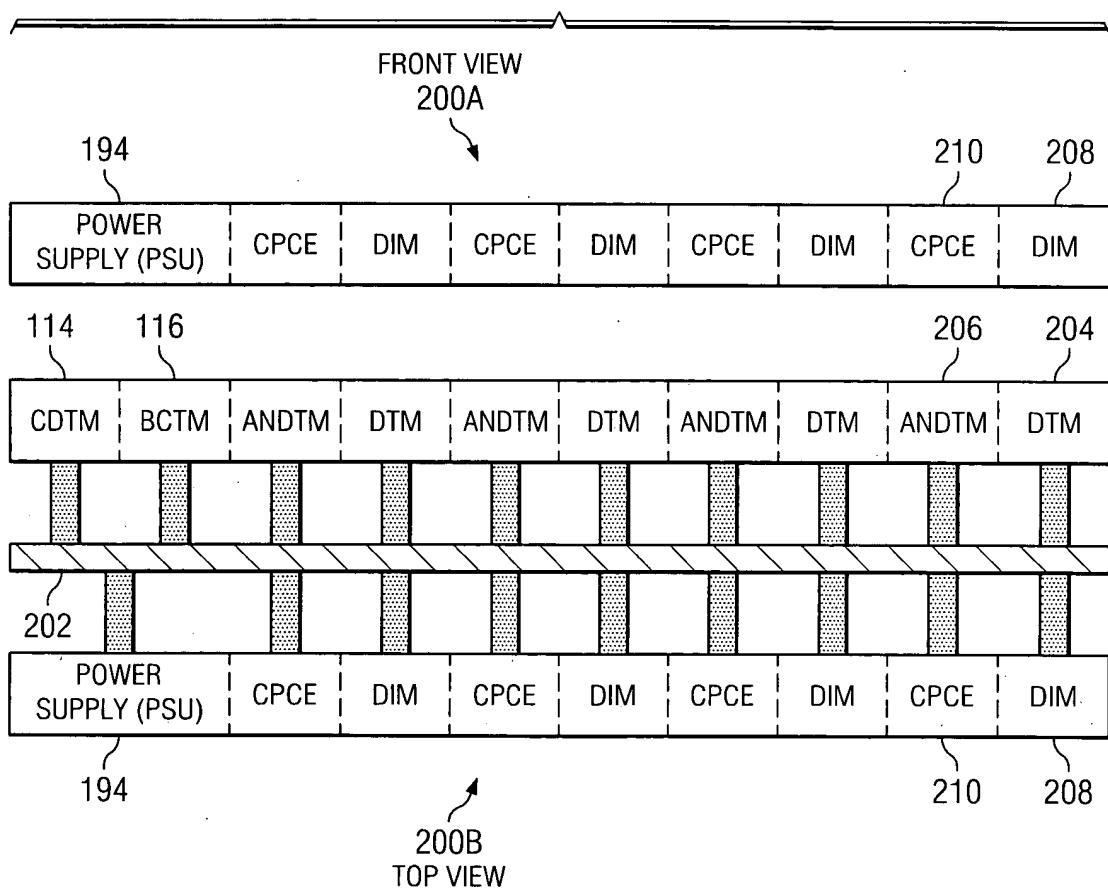


FIG. 10B

FIG. 10C

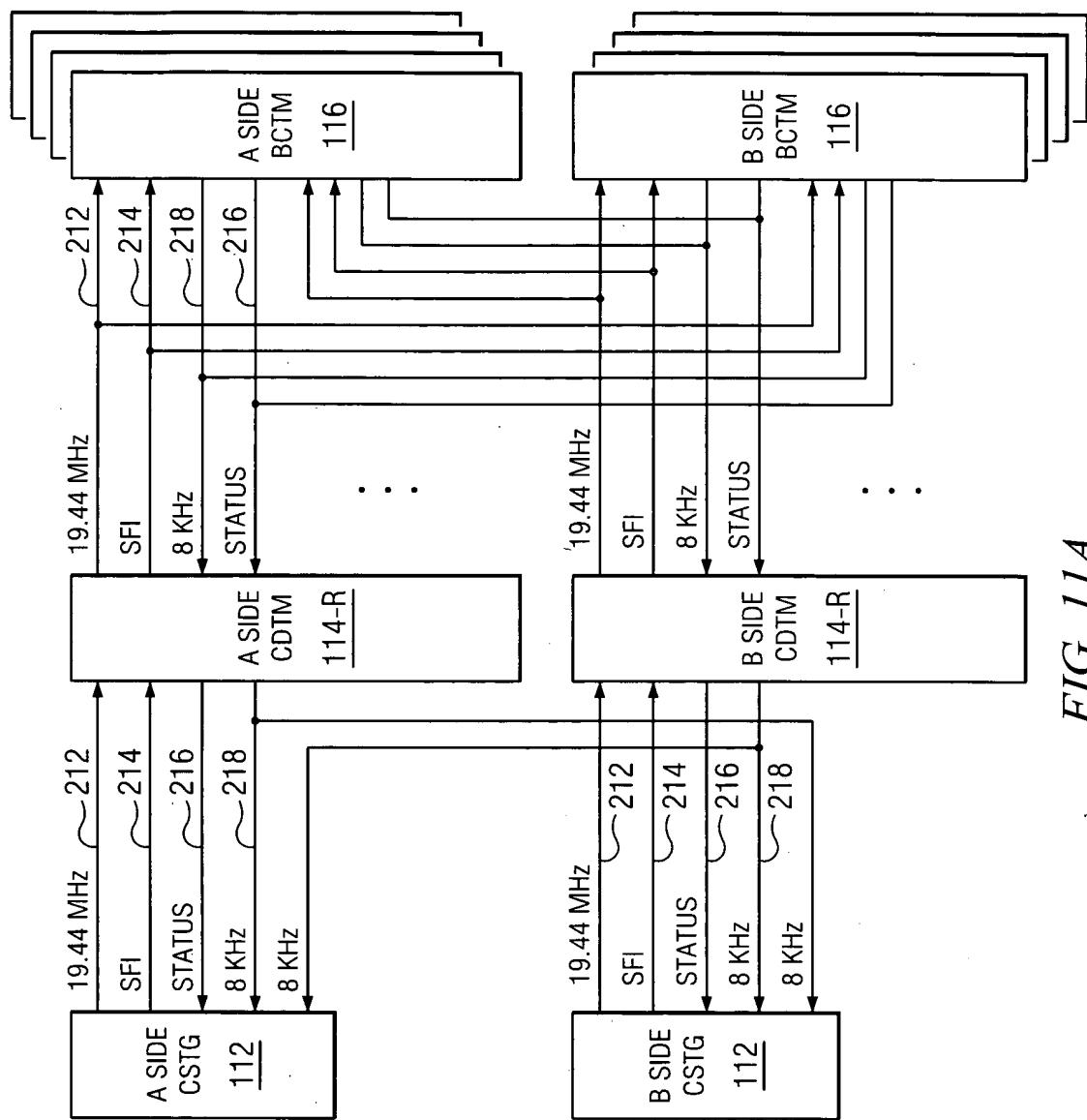


FIG. 11A

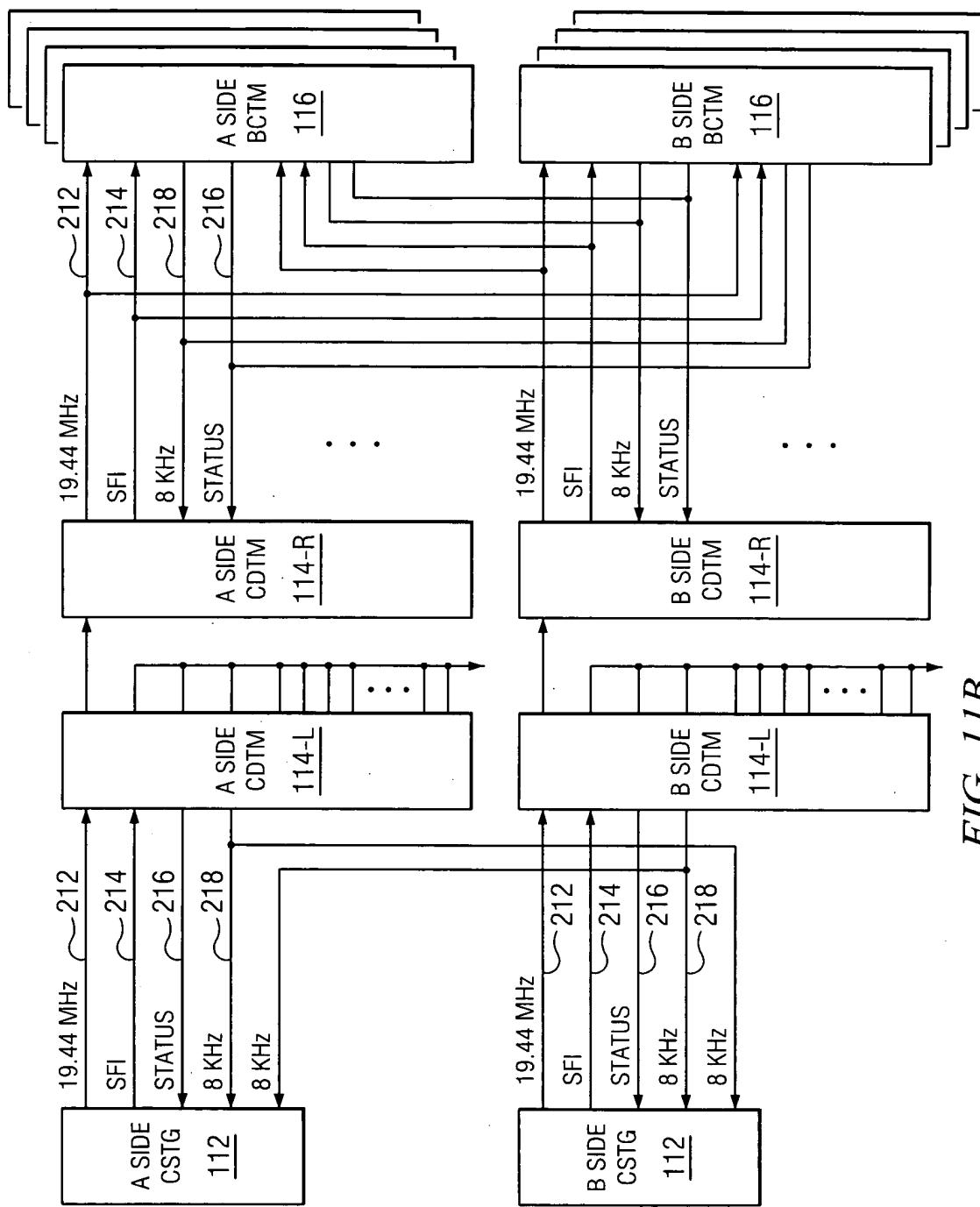


FIG. 11B

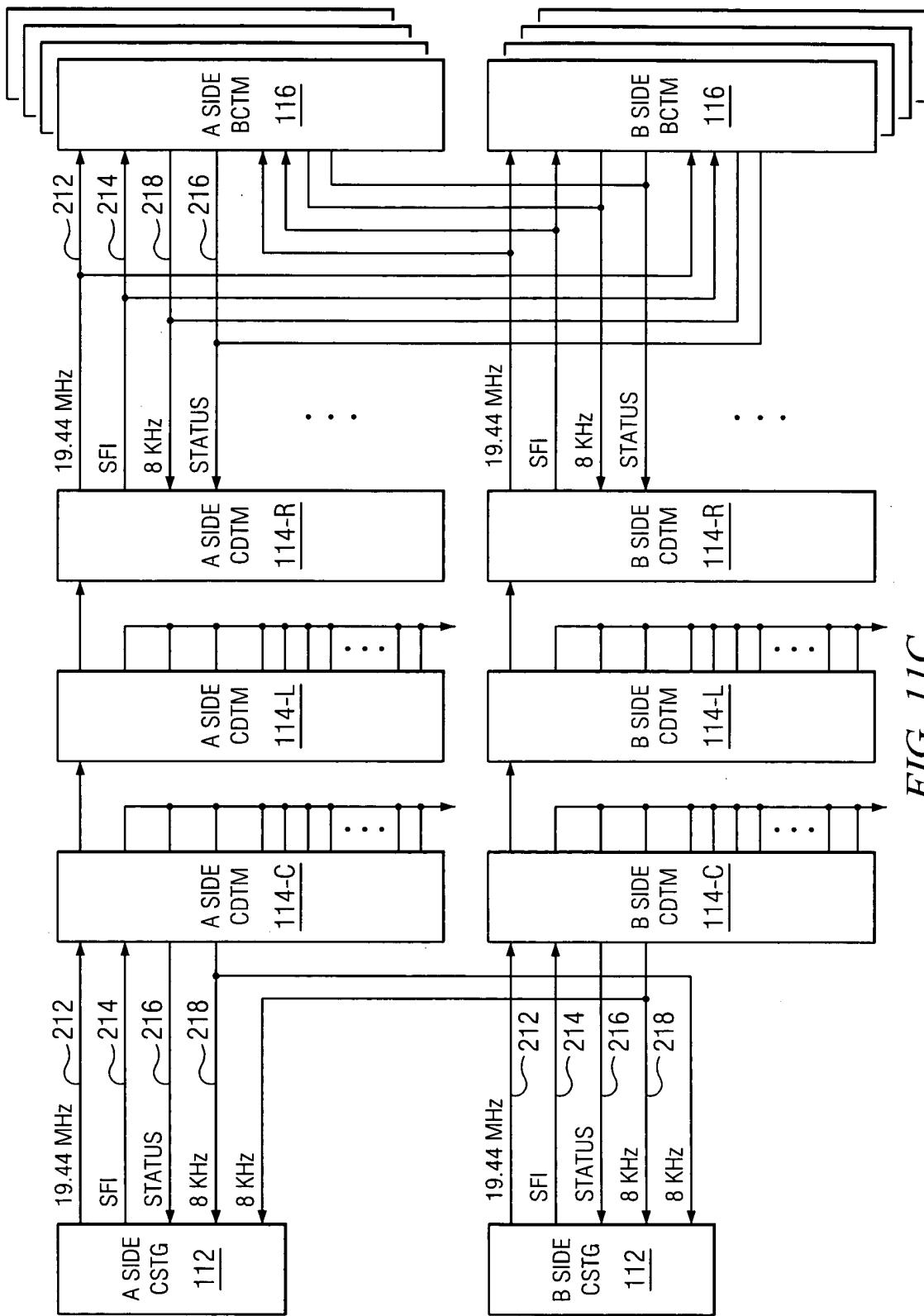


FIG. 11C

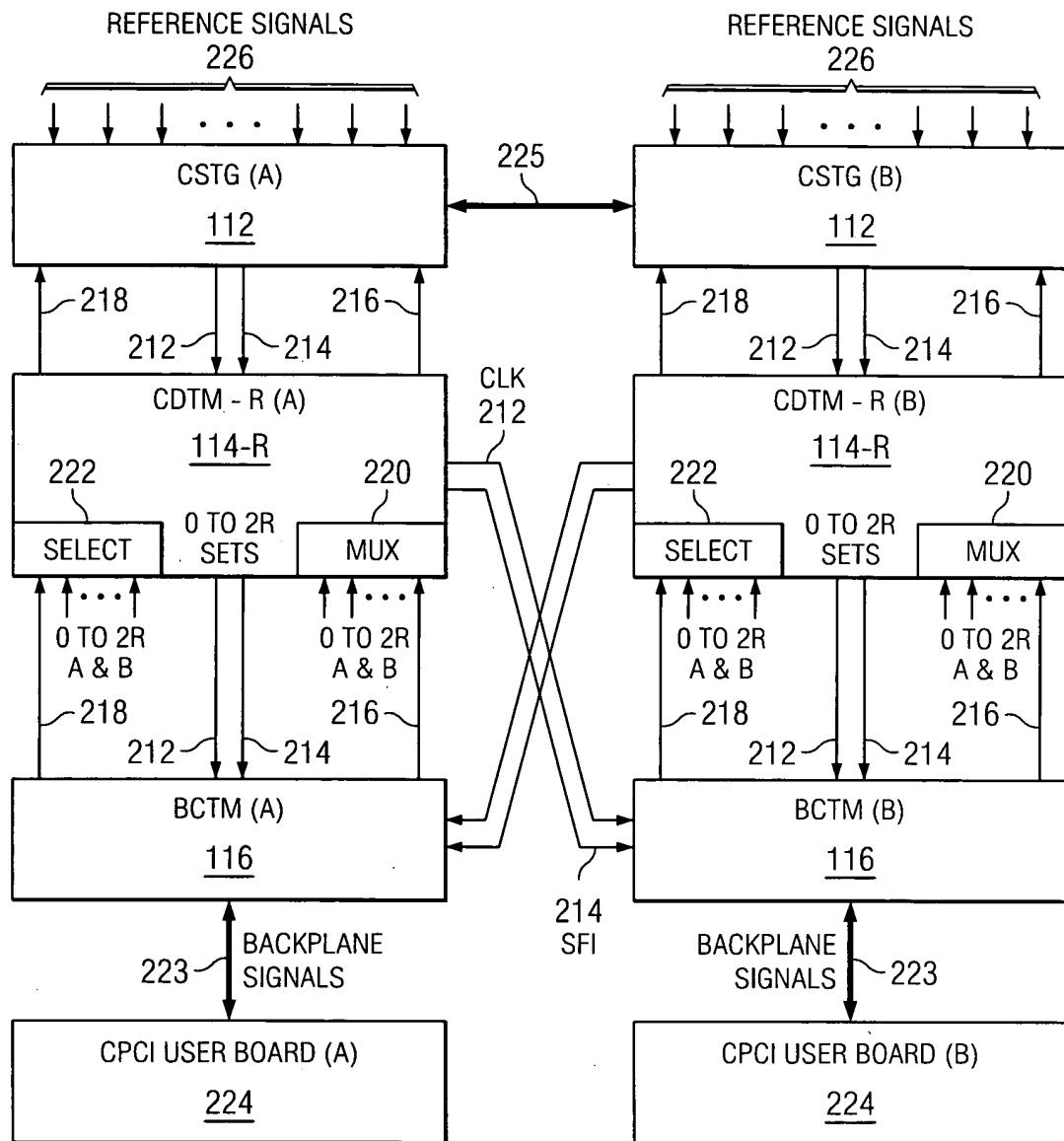


FIG. 12A

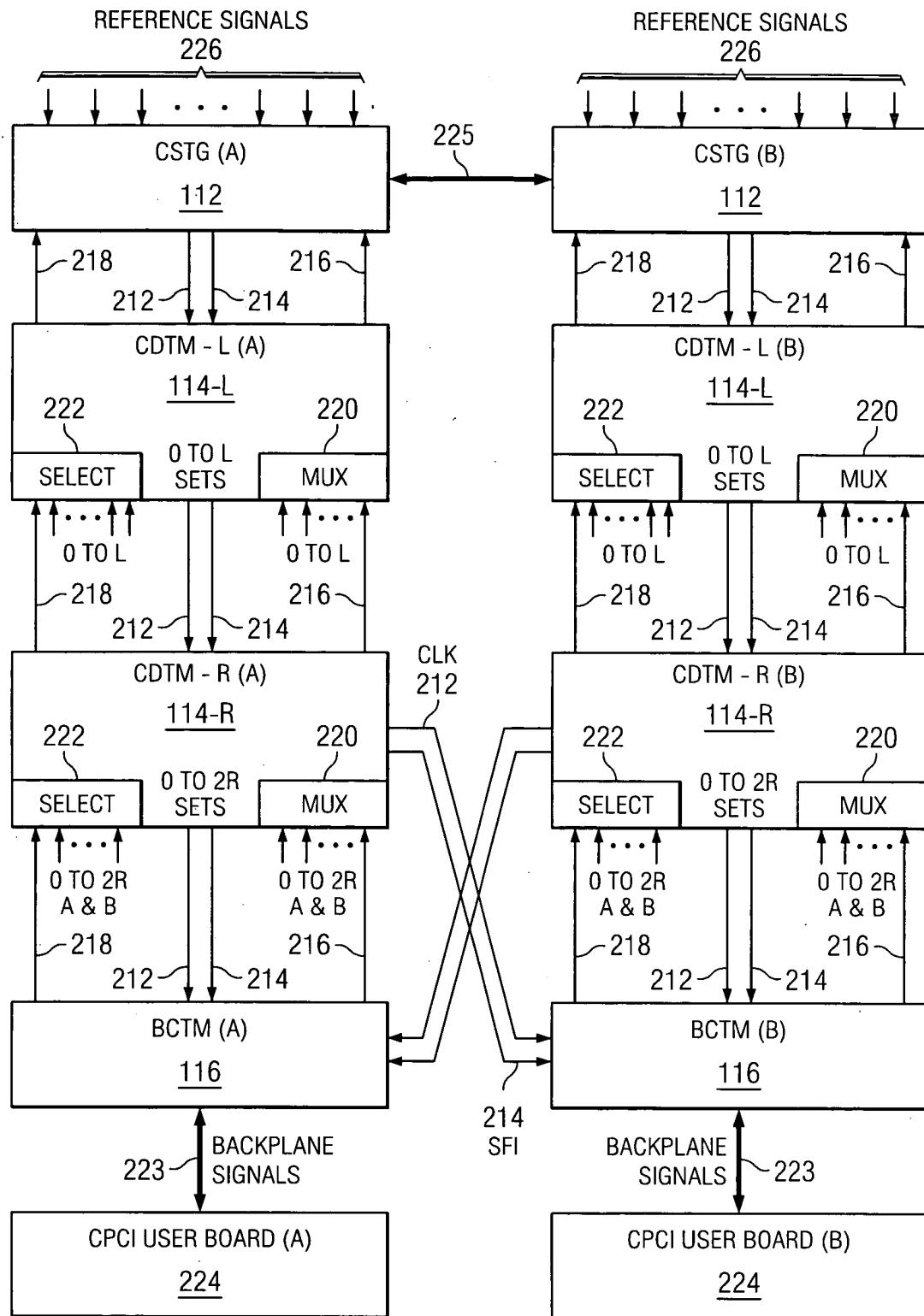


FIG. 12B

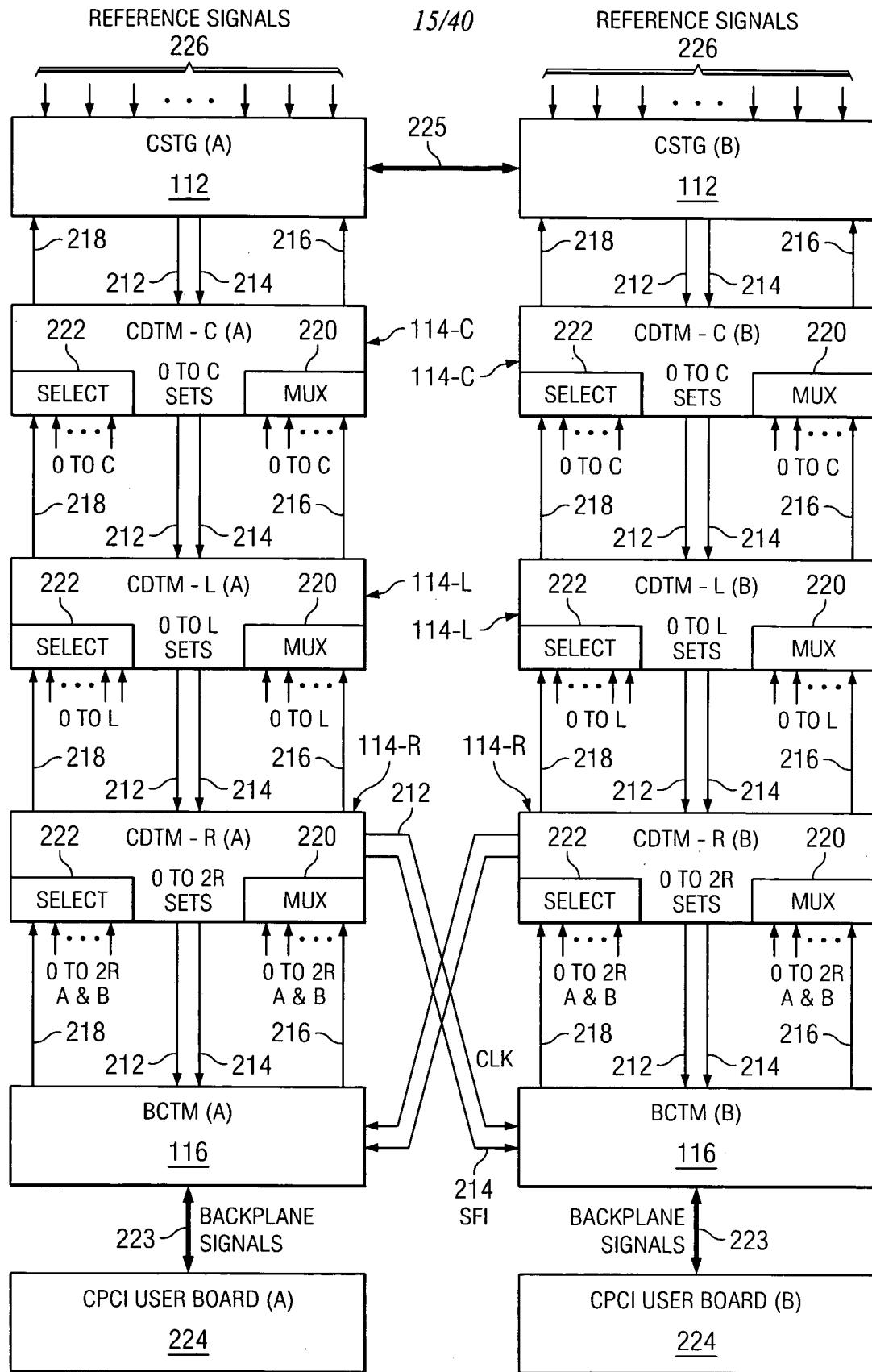


FIG. 12C

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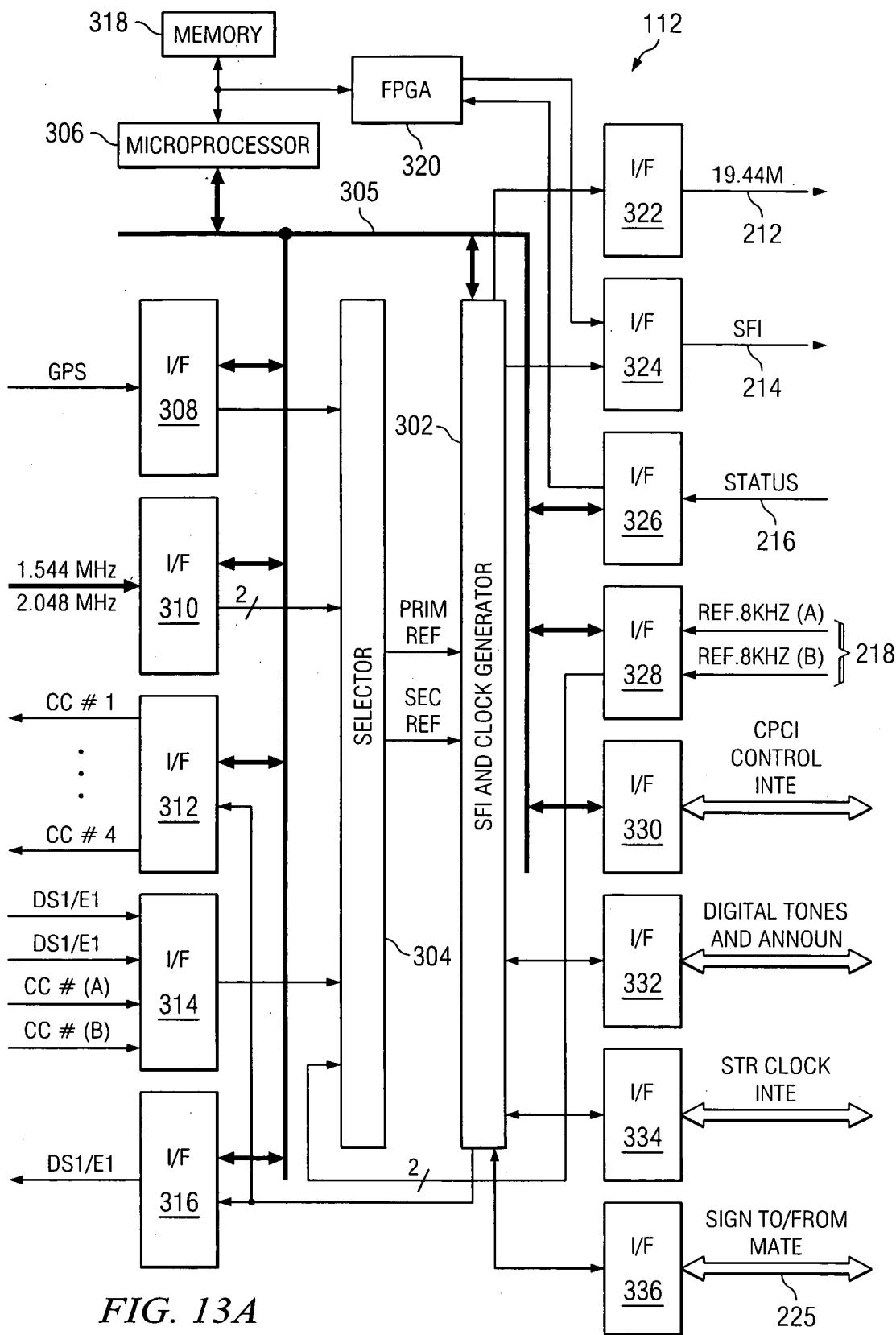


FIG. 13A

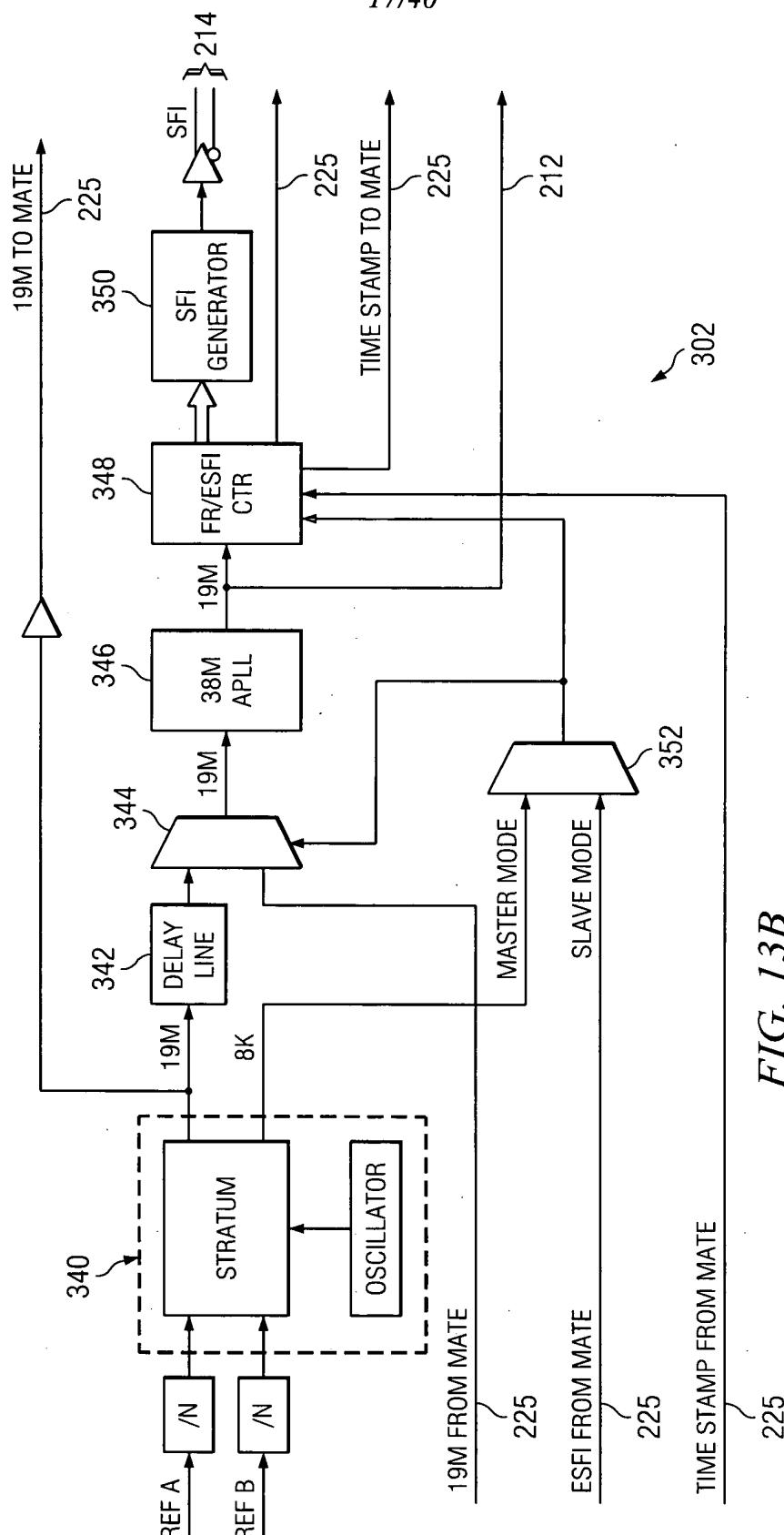


FIG. 13B

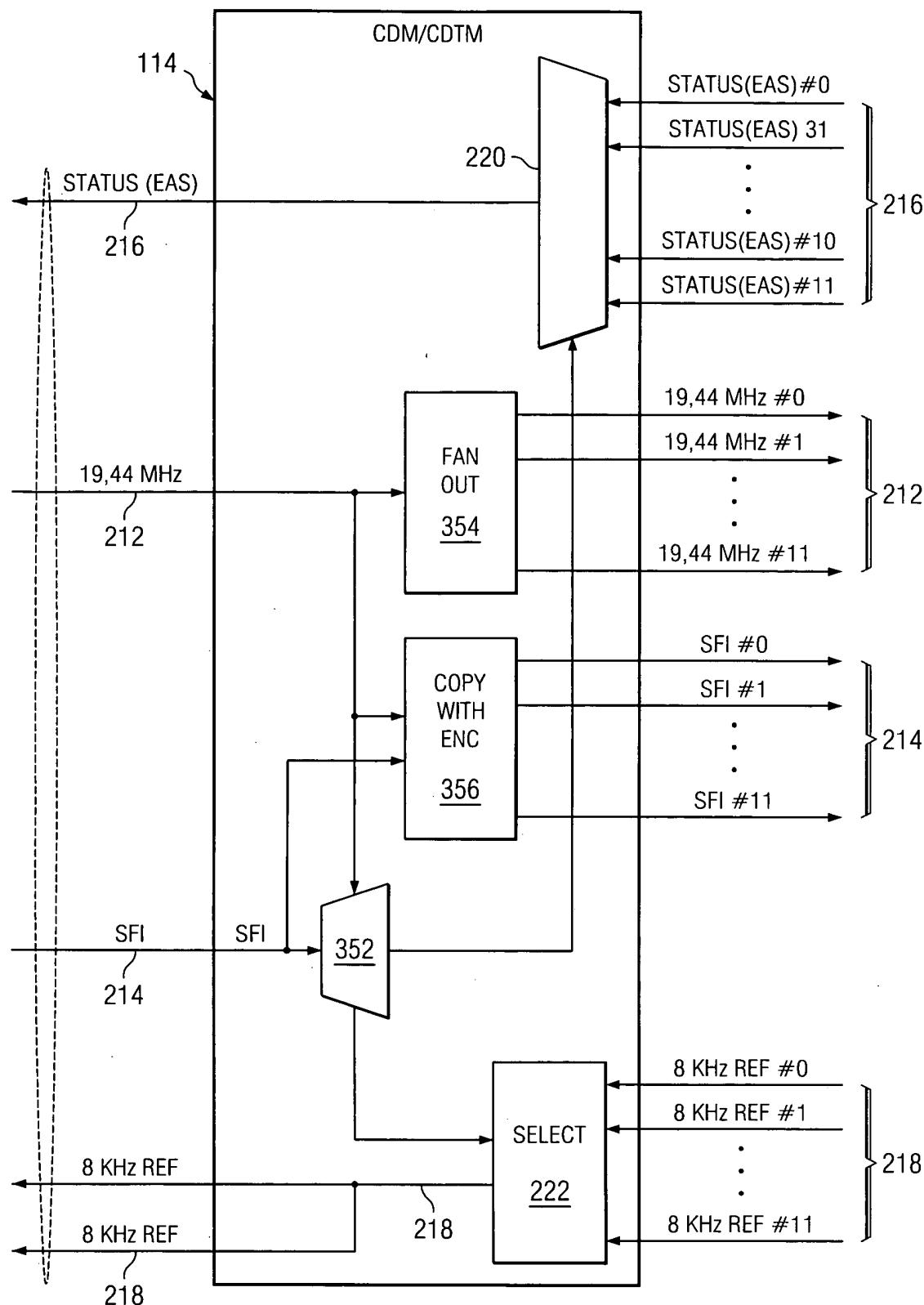
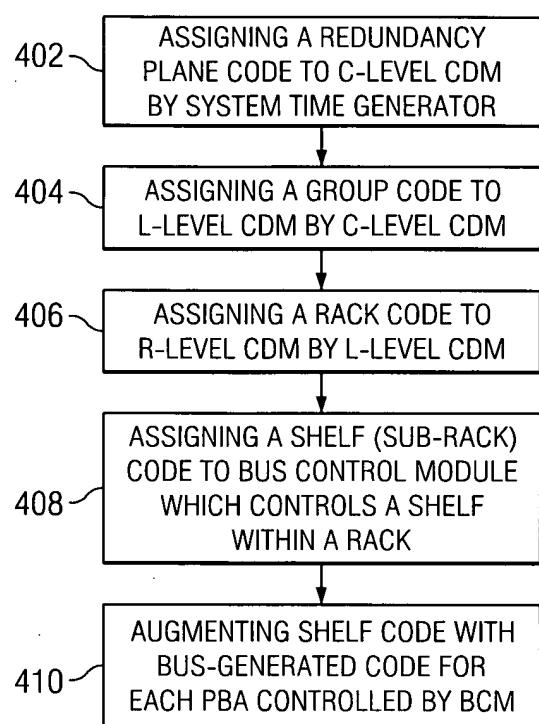


FIG. 14

FIG. 15

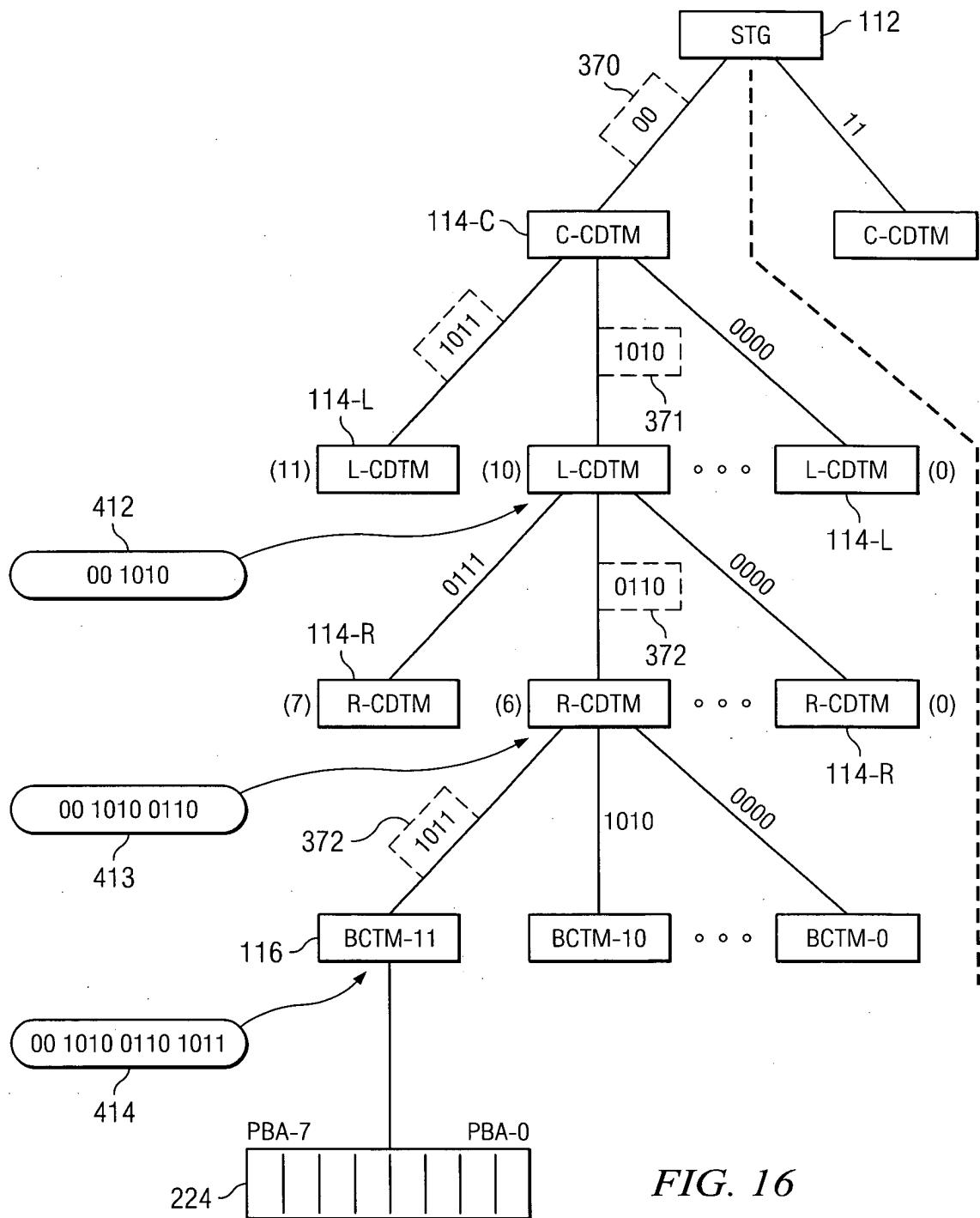


FIG. 16

FIG. 17A

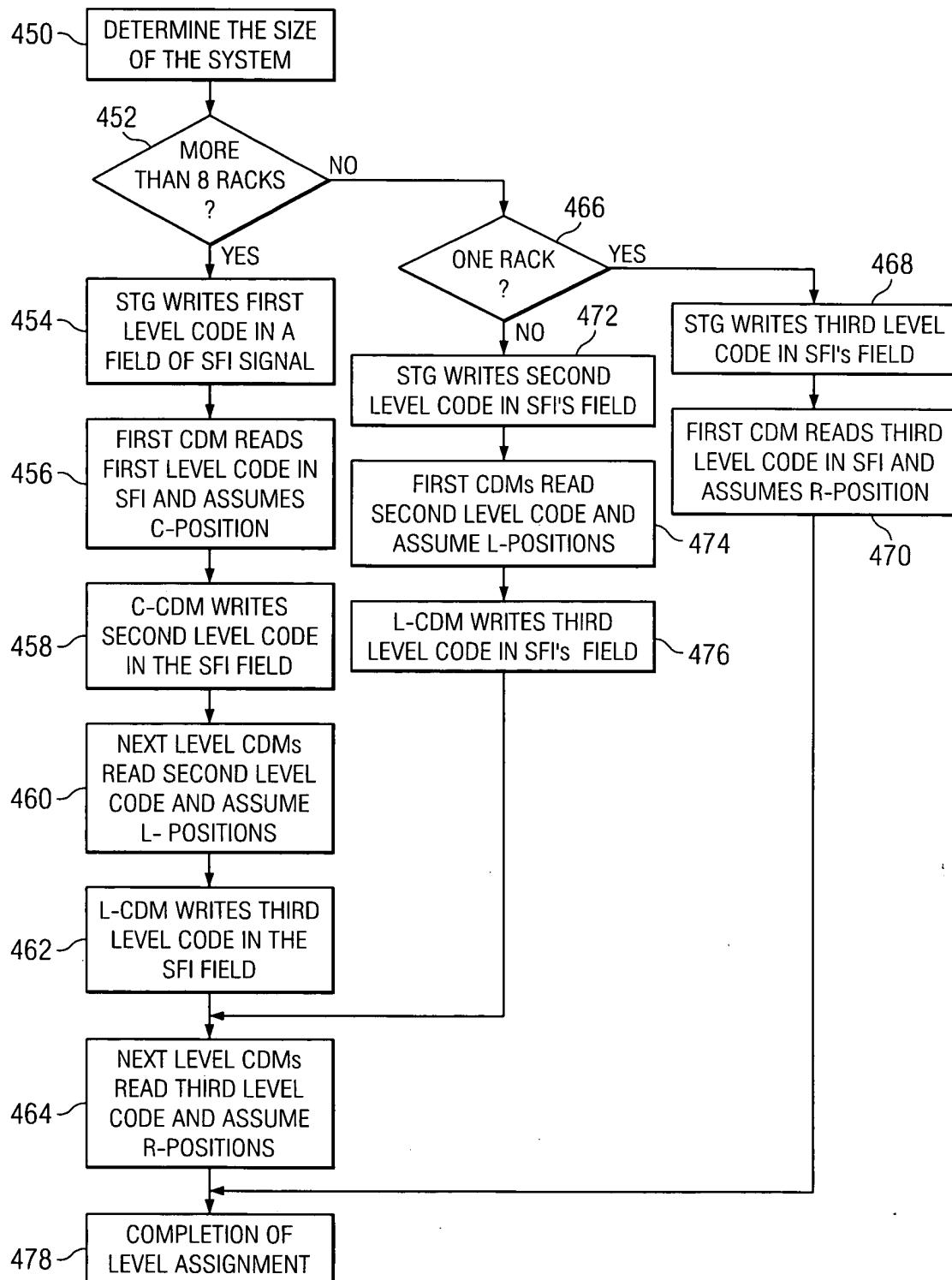
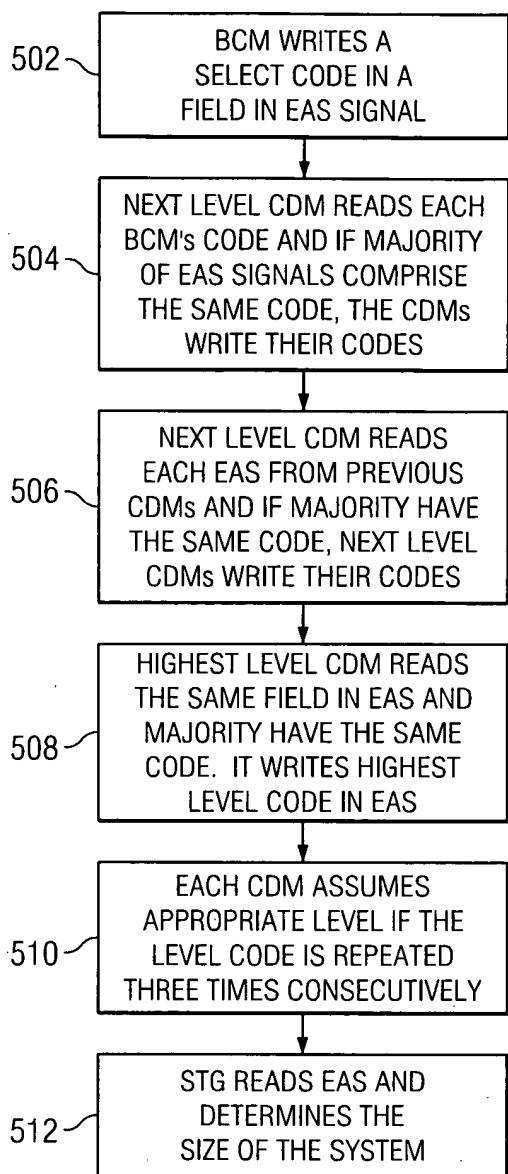
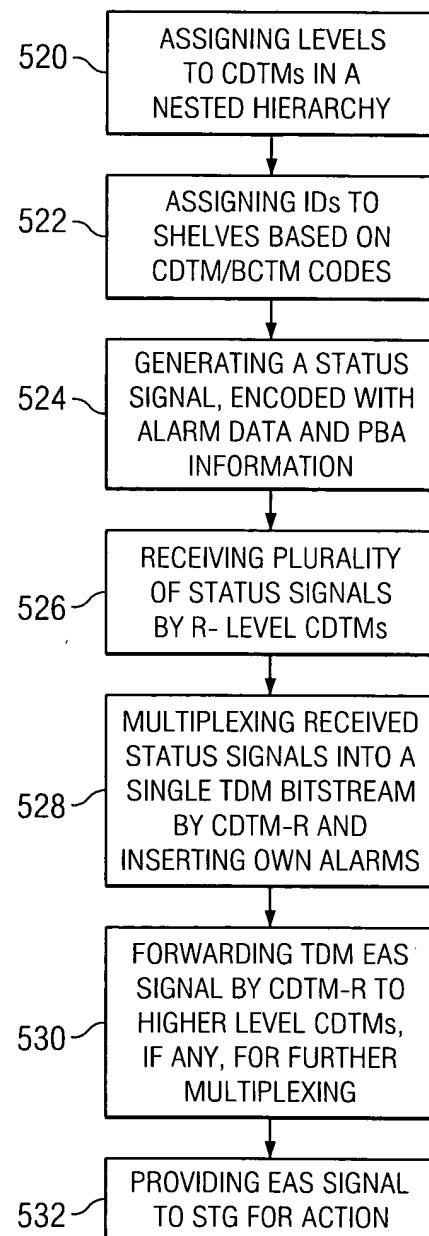
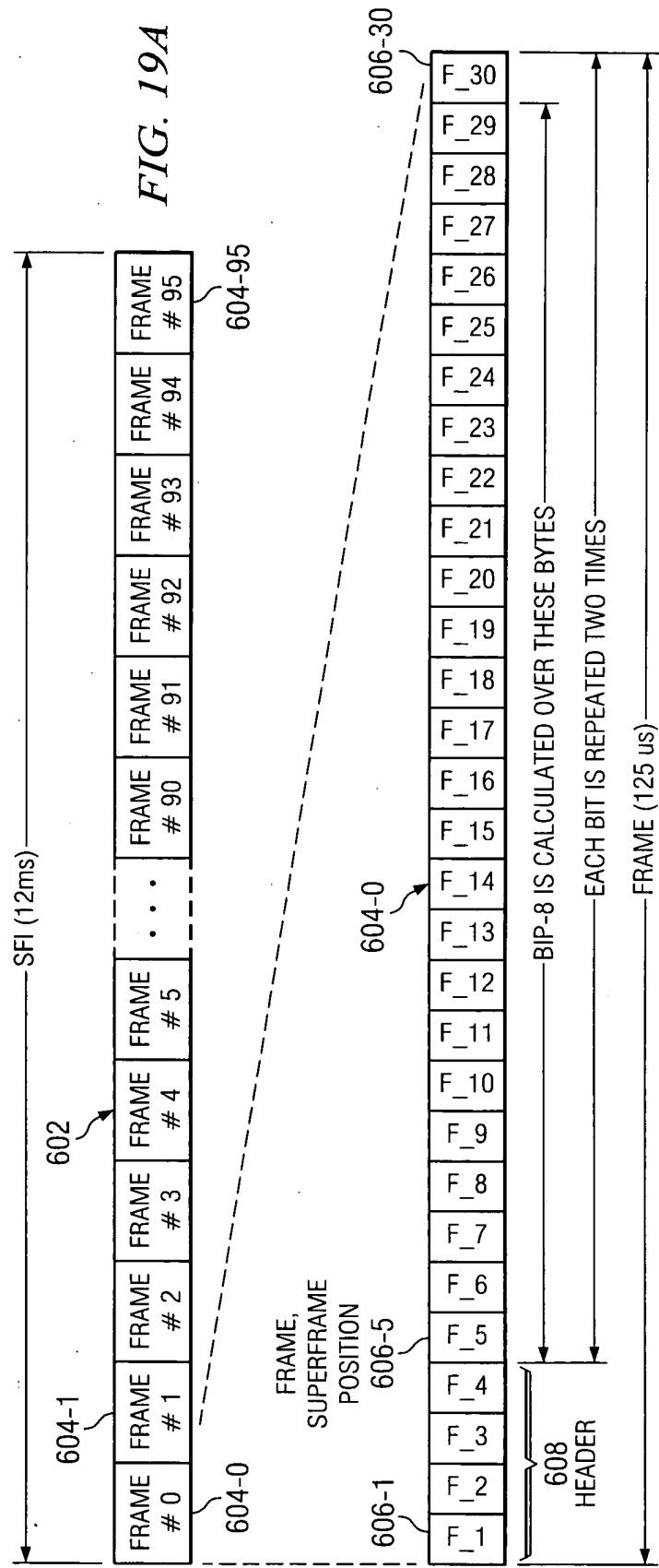


FIG. 17B**FIG. 18**

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FIG. 19A



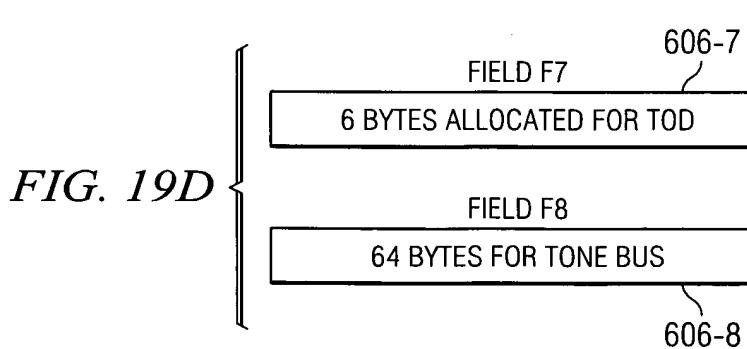
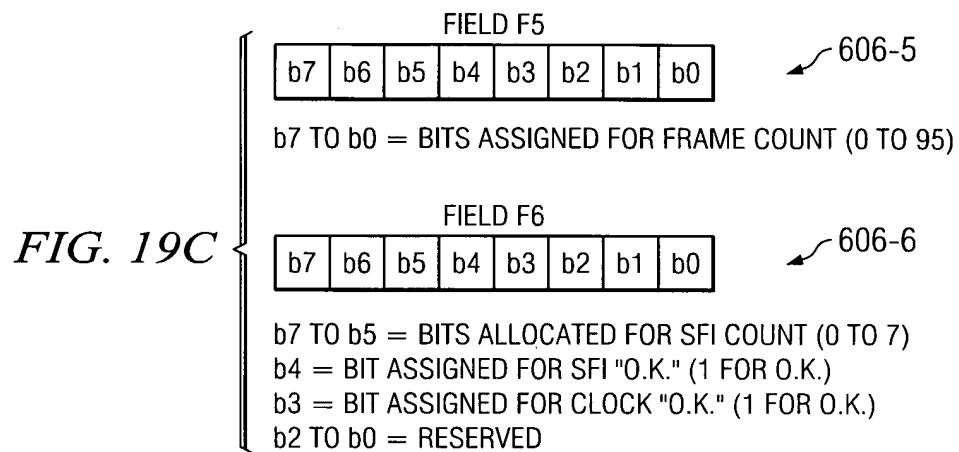
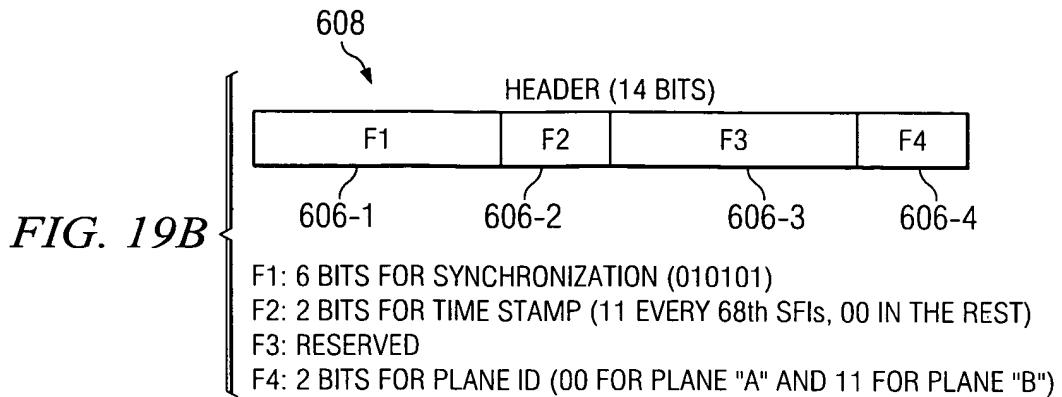


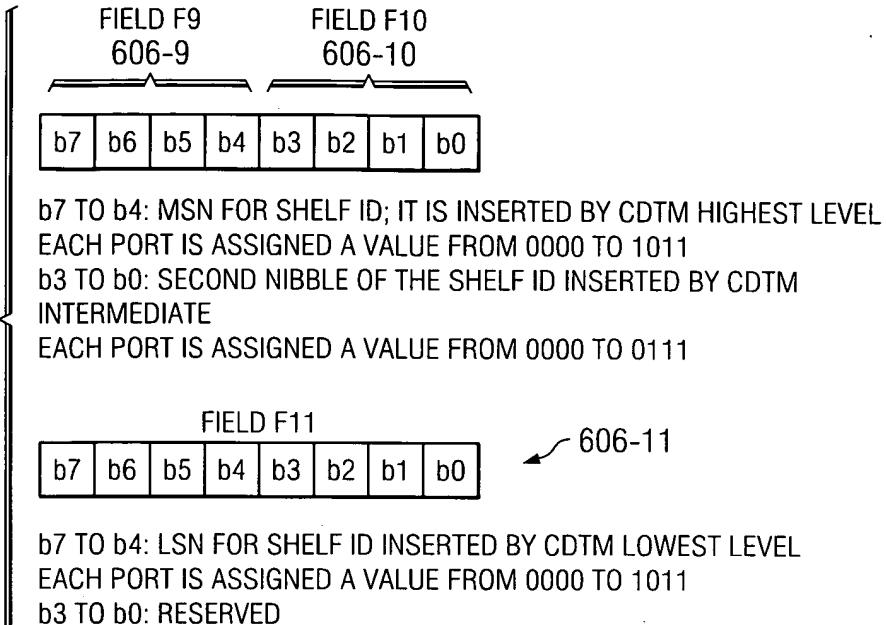
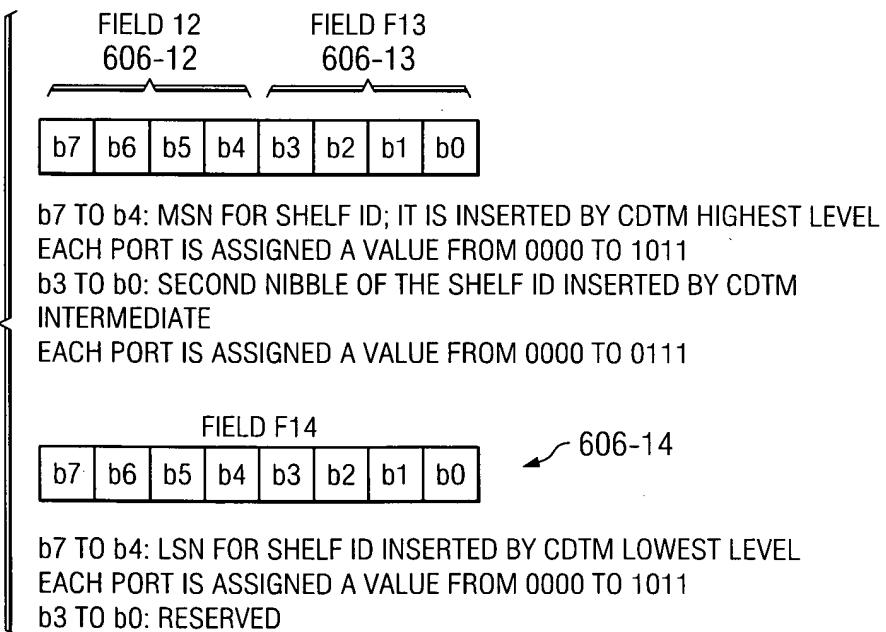
FIG. 19E**FIG. 19F**

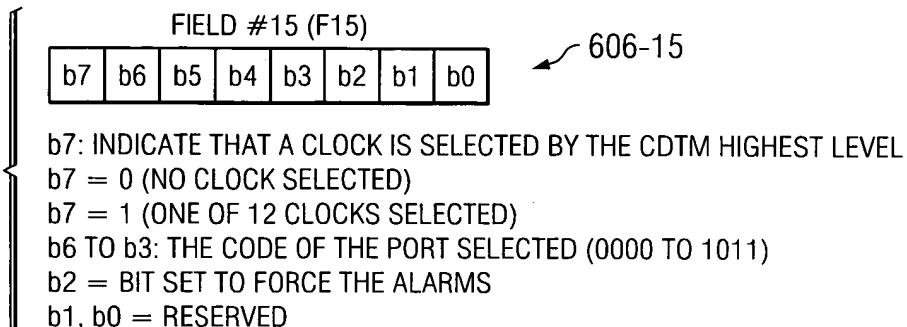
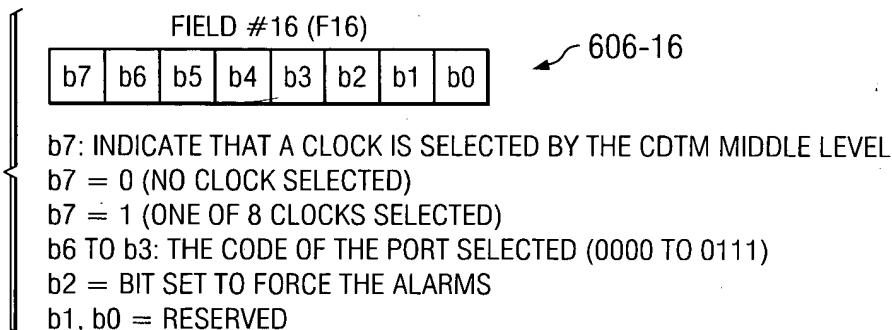
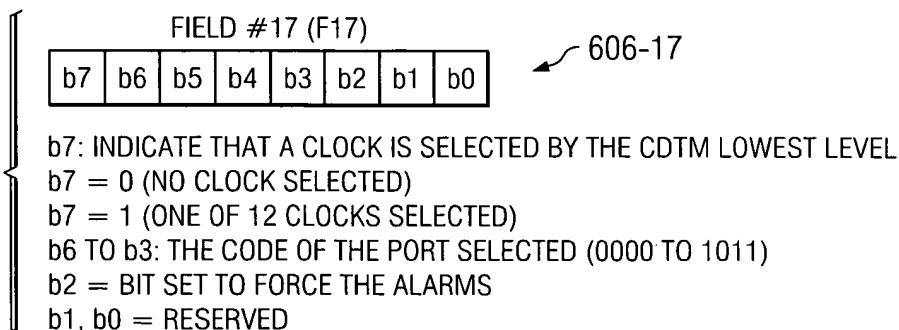
FIG. 19G*FIG. 19H**FIG. 19I*

FIG. 19J

FIELD #F18 TO #F27 606-18 TO 606-27

BYTE #1

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

609-1

b7 = 1 RESET SLOT #0	b3 = 1 RESET SLOT #4
b6 = 1 RESET SLOT #1	b2 = 1 RESET SLOT #5
b5 = 1 RESET SLOT #2	b1 = 1 RESET SLOT #6
b4 = 1 RESET SLOT #3	b0 = 1 RESET SLOT #7

BYTE #2

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

609-2

b7 = 1 RESET BCTM	b3 = BIT SET TO LOOP BACK 8 KHz
b6 = 1 RESET ESWT#1	b2 = I1
b5 = 1 RESET ESWT#2	b1 = I2
b4 = 1 RESET RESERVED	b0 = I3

BYTE #3

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

609-3

b7 = PWR DOWN SLOT #0	b3 = PWR DOWN SLOT #4
b6 = PWR DOWN SLOT #1	b2 = PWR DOWN SLOT #5
b5 = PWR DOWN SLOT #2	b1 = PWR DOWN SLOT #6
b4 = PWR DOWN SLOT #3	b0 = PWR DOWN SLOT #7

BYTE #4

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

609-4

b7 = 1: CLOCK SELECTED BY BCTM	b3 ,b2 = PLANE SELECTION (A OR B)
b6 TO b4: THE CODE OF SELECTED CLOCK (000 TO 111)	b1 = TURN ON/OFF RACK ALARM
	b0 = 1: FORCE SHELF ALARMS

FIG. 19K

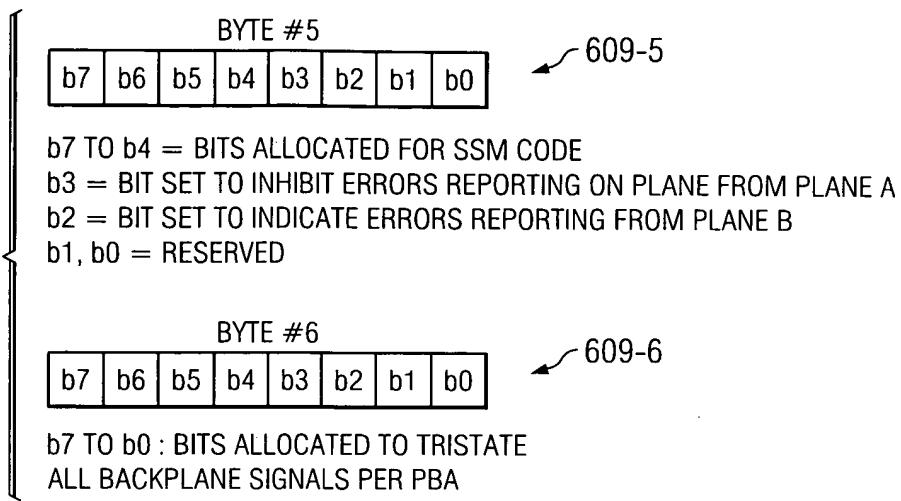


FIG. 19L

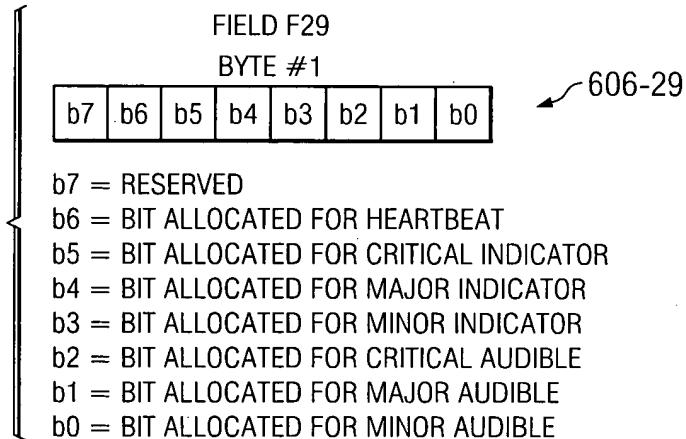
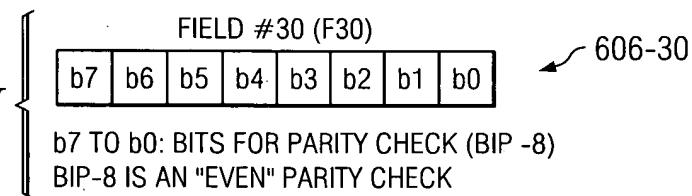


FIG. 19M



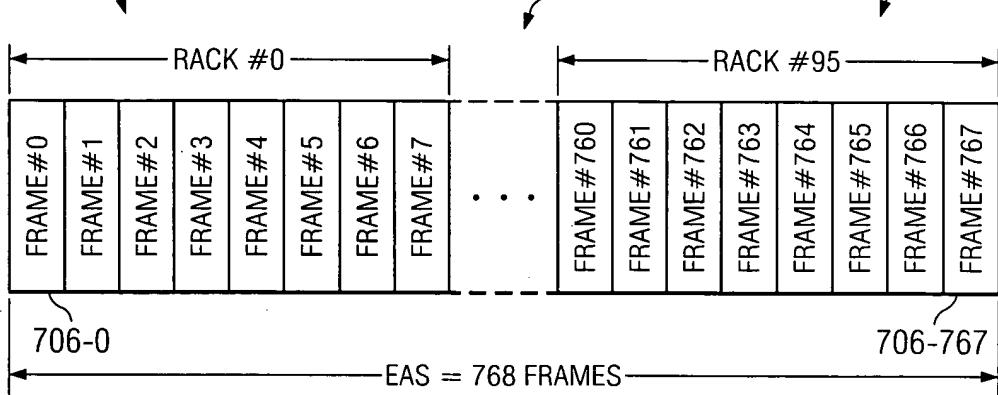
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704-0

FIG. 20A

702

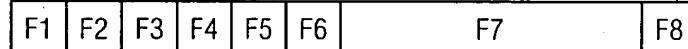
704-95



FRAME #0 (THIS FRAME IS ALLOCATED FOR CDTM HIGHEST LEVEL)

708-1 708-3 708-5

708-8



708-2 708-4 708-6 708-7

125 MICROSECONDS

706-0

FIG. 20B

F1 = 1 BYTE ALLOCATED FOR ALIGNMENT PATTERN

F2 = 1 BYTE ALLOCATED FOR MISC SIGNALS

F3 = 2 BYTES ALLOCATED FOR STATUS LOA

F4 = 2 BYTES ALLOCATED FOR INBOUND ERRORS (ONE BIT PER PORT)

F5 = 1 BYTE ALLOCATED FOR CLOCK SELECTION

F6 = 4 BITS ALLOCATED FOR BIP-4

F7 = 7.5 BYTES RESERVED

F8 = 1.5 BITS EMPTY FIELD (FORCED TO "0")

NOTE: ALL FRAMES # N x 64 (N = 1 TO 11) ARE EMPTY

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FIG. 20C

FRAME #0

FIELD F1

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

708-1

b7 TO b0 = "00111100" : PATTERN GENERATED BY EACH CDTM AND BCTM CARDS

FIELD F2

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

708-2

b7, b6 = RESERVED

b5 = BIT SET WHEN OUTBOUND SFI ERRORS ARE DETECTED

NOTE: OUTBOUND ERRORS: "TWO BITS VIOLATIONS" OR BIP-8 ONLY FOR FRAME #0 OF THE SFI

b4 = BIT SET WHEN ONE OF THE STATUS SIGNAL IS IN LOS (EXCEPT FOR THE UNUSED PORTS)

b3 = BIT SET WHEN THE SELECTED 8KHz IS IN LOC

b2, b1 = BIT ASSIGNED TO INDICATE THE LEVEL OF THE CDTM (11 FOR L1, 10 FOR L2 AND 01 FOR L3)

b0 = BIT ASSIGNED TO INDICATE THE PLANE (0 FOR A AND 1 FOR B)

FIELD F3 (BYTE #1)

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

708-3

b7 TO b0 = BITS ASSIGNED TO INDICATE STATUS LOA PORTS #0 TO 7

FIELD F3 (BYTE #2)

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

708-3

b3 TO b0 = BITS ASSIGNED FOR STATUS LOA #8 TO 12

b7 TO b4 = BITS ALLOCATED FOR CDTM LEVEL ASSIGNMENT

FIG. 20D

FIELD F4 (BYTE #1)

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

708-4

b7 TO b0 = BITS ASSIGNED TO INDICATE STATUS LOS ON PORTS #0 TO 7

FIELD F4 (BYTE #2)

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

708-4

b7 TO b0 = BITS ASSIGNED TO INDICATE STATUS LOS ON PORTS #8 TO 12

FIELD F5

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

708-5

b7 = BIT SET WHEN AN 8 KHz INPUT WAS SELECTED

b6, b5, b4, b3 = BITS ASSIGNED TO INDICATE WHICH 8KHz PORT WAS SELECTED

b2 = BIT SET WHEN A FORCED ALARM BIT WAS RECEIVED

b1 = BIT ASSERTED BY CDTM HIGHEST LEVEL TO INDICATE THAT A SYSTEM ALARM HAS OCCURED IN THE PREVIOUS EAS

b0 = ASSERTED BY CSTG IF A BIP-4 ERROR WAS DETECTED

FIELD F6

b3	b2	b1	b0
----	----	----	----

708-6

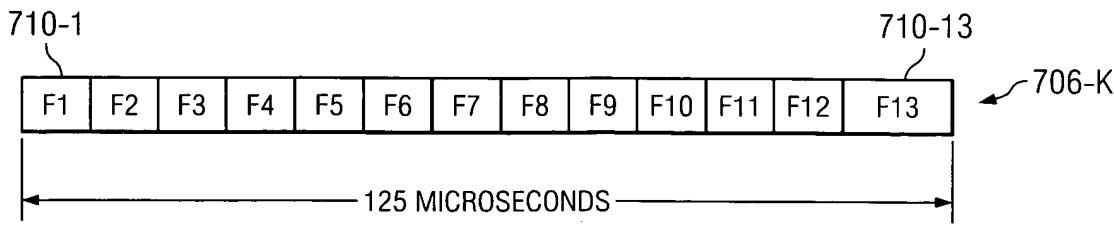
b3 TO b0 = BITS ASSIGNED FOR BIP-4

FIG. 20E

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FRAME #1 + 64 x N (N = 0 TO 11) : THESE FRAMES
ARE ALLOCATED FOR CDTM MIDDLE LEVEL

FRAME #1 + 8 x N (N = 0 TO 95) : THESE FRAMES
ARE ALLOCATED FOR CDTM LOWEST LEVEL



CDTM MIDDLE LEVEL

F1 = 1 BYTE ALLOCATED FOR MISC SIGNALS

F2 = 1 BYTE FOR CARD ID

F3 = 2 BYTES ALLOCATED FOR STATUS LOA

F4 = 2 BYTES ALLOCATED FOR INBOUND ERRORS (ONE BIT PER PORT)

F5 = 1 BYTE ALLOCATED FOR CLOCK SELECTION

F6 = 4 BYTES ALLOCATED FOR BIP-4

CDTM LOWEST LEVEL

F7 = 1 BYTE ALLOCATED FOR MISC SIGNALS

F8 = 1 BYTE FOR CARD ID

F9 = 2 BYTES ALLOCATED FOR STATUS LOA

F10 = 2 BYTES ALLOCATED FOR INBOUND ERRORS

F11 = 1 BYTE ALLOCATED FOR CLOCK SELECTION

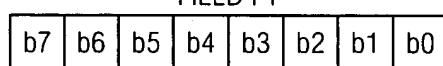
F12 = 4 BITS ALLOCATED FOR BIP-4

F13 = 1.5 BITS EMPTY FIELD (FORCED TO "0")

FIG. 20F

FRAME #1 + 64 x N (N = 0 TO 11) : THESE FRAMES
ARE ALLOCATED FOR CDTM MIDDLE LEVEL

FIELD F1



b7 = RESERVED

b6 = RESERVED

b5 = BIT SET WHEN OUTBOUND SFI ERRORS ARE DETECTED

NOTE: OUTBOUND ERRORS: "TWO BITS VIOLATIONS" OR BIP-8 ONLY FOR FRAME #0 OF THE SFI

b4 = BIT SET WHEN ONE OF THE STATUS SIGNAL IS IN LOS (EXCEPT FOR THE UNUSED PORTS)

b3 = BIT SET WHEN THE SELECTED 8KHz IS IN LOC

b2, b1 = BIT ASSIGNED TO INDICATE THE LEVEL OF THE CDTM (11 FOR L1, 10 FOR L2 AND 01 FOR L3)

b0 = BIT ASSIGNED TO INDICATE THE PLANE (0 FOR A AND 1 FOR B)

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FIG. 20G

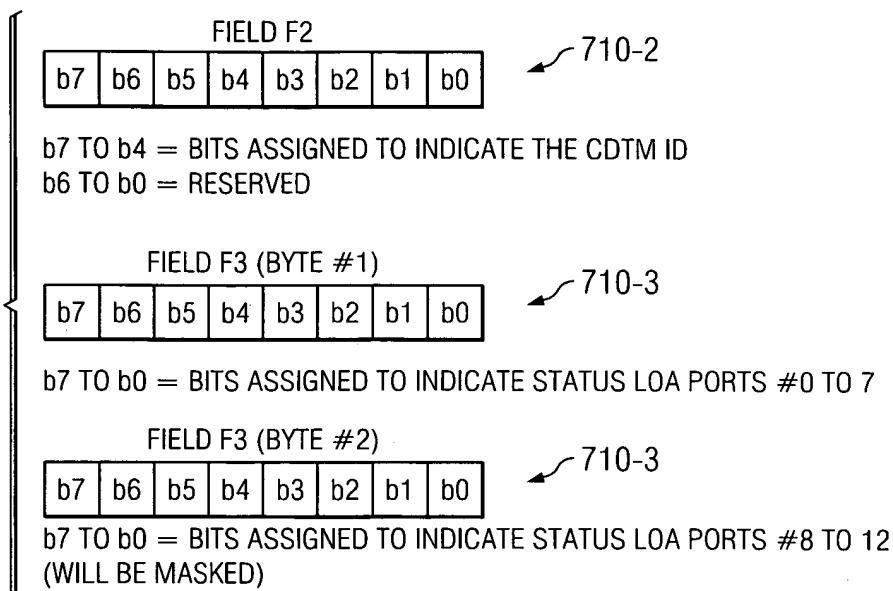


FIG. 20H

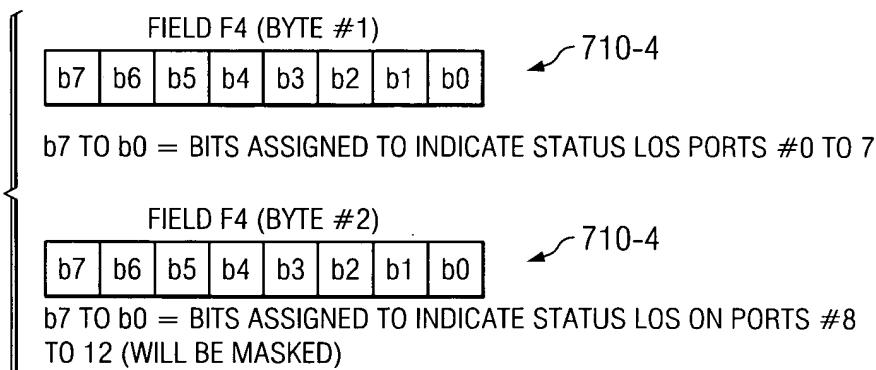


FIG. 20I

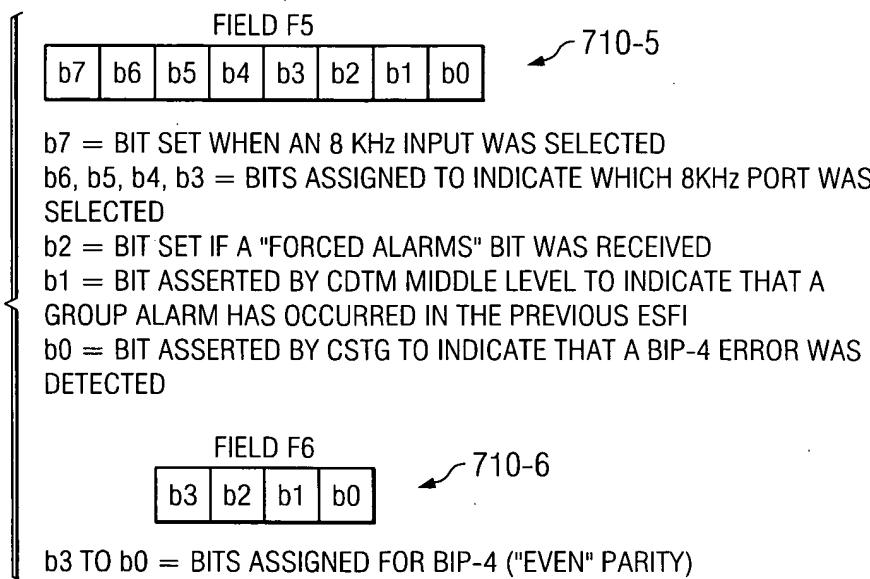


FIG. 20J

FRAME# 1 + 8 x N (N = 0 TO 95) : THESE FRAMES ARE ALLOCATED FOR CDTM LOWEST LEVEL

FIELD F7

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

710-7

b7 = RESERVED

b6 = RESERVED

b5 = BIT SET WHEN OUTBOUND SFI ERRORS ARE DETECTED

NOTE: OUTBOUND ERRORS: "TWO BITS VIOLATIONS" OR BIP-8 ONLY FOR FRAME #0 OF THE SFI

b4 = BIT SET WHEN ONE OF THE STATUS SIGNAL IS IN LOS (EXCEPT FOR THE UNUSED PORTS)

b3 = BIT SET WHEN THE SELECTED 8KHz IS IN LOC

b2, b1 = BIT ASSIGNED TO INDICATE THE LEVEL OF THE CDTM (11 FOR L1, 10 FOR L2 AND 01 FOR L3)

b0 = BIT ASSIGNED TO INDICATE THE PLANE (0 FOR A AND 1 FOR B)

FIG. 20K

FIELD F8

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

710-8

b7 TO b0 = BITS ASSIGNED FOR CDTM ID

FIELD F9 (BYTE #1)

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

710-9

b7 TO b0 = BITS ASSIGNED TO INDICATE STATUS LOA PORTS #0 TO 7

FIELD F9 (BYTE #2)

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

710-9

b7 TO b0 = BITS ASSIGNED TO INDICATE STATUS LOA PORTS #8 TO 15

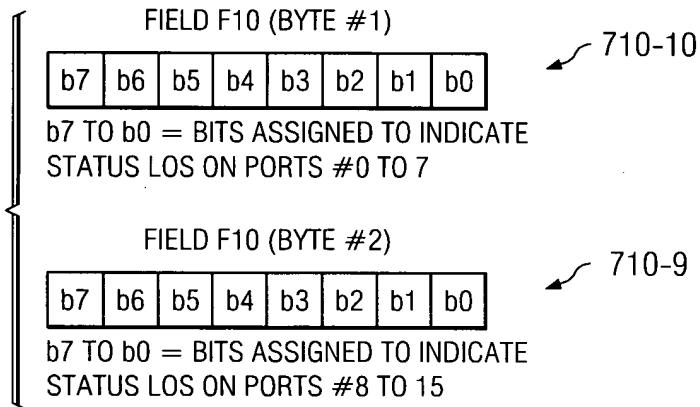
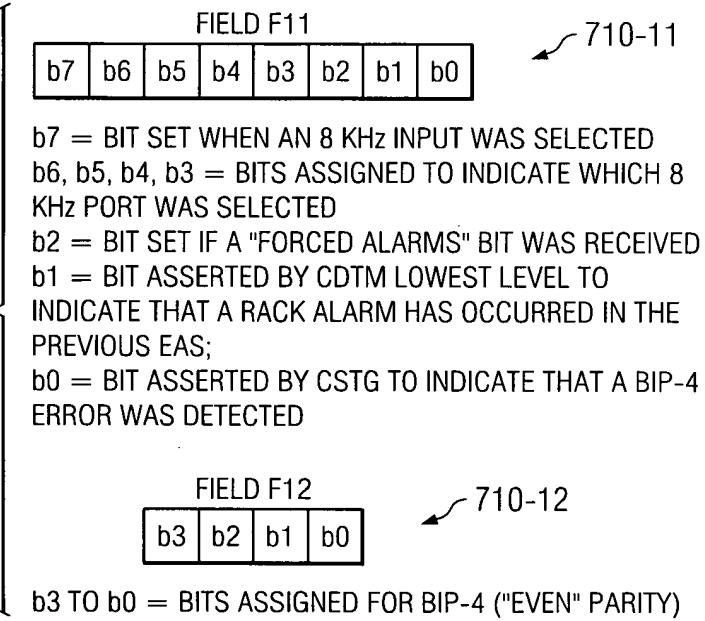
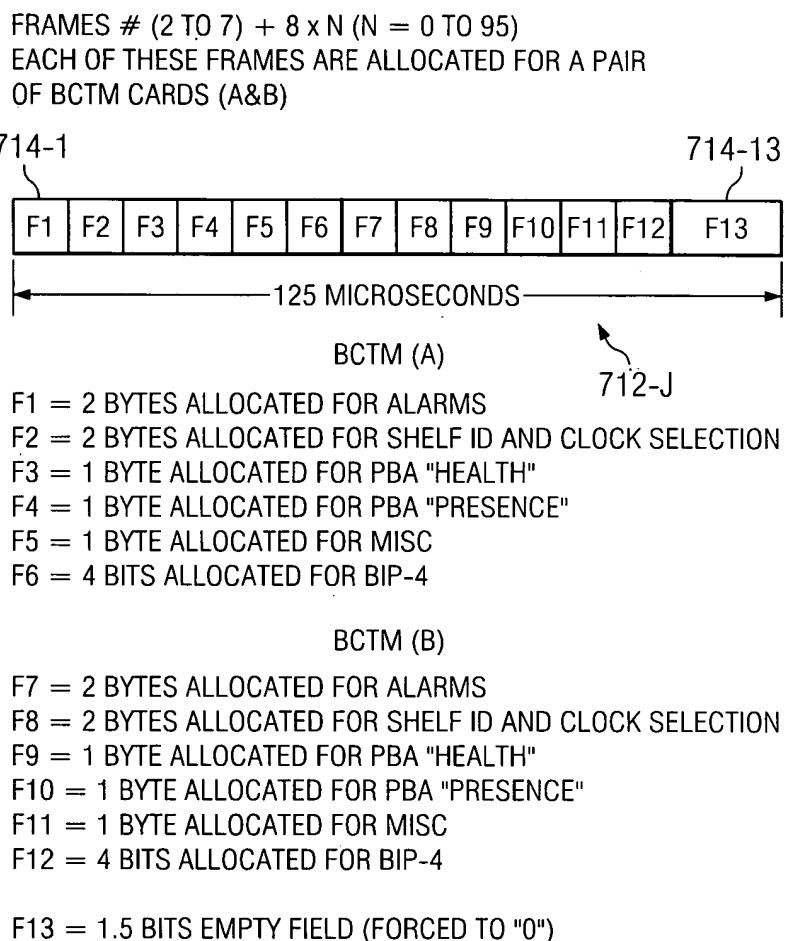
FIG. 20L*FIG. 20M*

FIG. 20N



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BCTM (A)
FIELD F1 (BYTE #1)

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

714-1

b7, b6 = BITS ALLOCATED FOR PSU #1 (A) ALARM
 b5, b4 = BITS ALLOCATED FOR PSU #2 (B) ALARM
 b3, b2 = BITS ALLOCATED FOR TWO FAN ALARMS
 b1, b0 = BITS ALLOCATED FOR ESWT PWR ALARM

FIELD F1 (BYTE #2)

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

714-1

FIG. 20O

b7 = BITS ALLOCATED FOR BREAKER PANEL ALARM
 b6 = BIT ALLOCATED FOR TEMP CONTROL
 b5 = BIT ALLOCATED TO INDICATE PLANE A LOST (LOC, LOS, SFI LOF)
 b4 = BIT ALLOCATED TO INDICATE PLANE B LOST (LOC, LOS, SFI LOF)
 b3 = BIT ALLOCATED TO INDICATE "TWO BITS VIOLATION" OR BIP-8
 ERRORS PLANE A (ONLY FOR THE FRAME ALLOCATED FOR THAT
 PARTICULAR RACK)
 b2 = BIT ALLOCATED TO INDICATE "TWO BITS VIOLATION" OR BIP-8
 ERRORS PLANE B (ONLY FOR THE FRAME ALLOCATED FOR THAT
 PARTICULAR RACK)
 b1 = ONE BIT TO INDICATE THAT A FORCED ALARM BIT WAS RECEIVED
 b0 = RESERVED

FIELD F2 (BYTE #1)

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

714-2

b7 TO b0 = HIGHER 8 BITS OF THE BCTM ID

FIELD F2 (BYTE #2)

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

714-2

FIG. 20P

b7 TO b4 = LOWER 4 BITS OF THE BCTM ID
 b3 = BIT SET TO INDICATE THAT AN 8 KHz CLOCK WAS SELECTED
 b2 TO b0 = BITS ALLOCATED TO INDICATE WHICH CLOCK WAS
 SELECTED

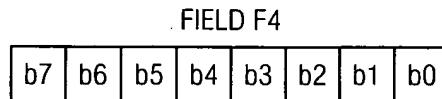
FIELD F3

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

714-3

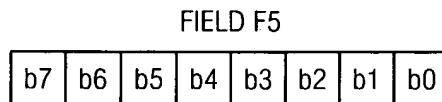
b7 TO b0 = BITS ALLOCATED TO INDICATE PBA "HEALTH" FROM
SLOT #0 TO SLOT #7

FIG. 20Q



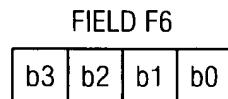
714-4

b7 TO b0 = BITS ALLOCATED TO INDICATE PBA
"PRESENCE" FROM SLOT #0 TO SLOT #7



714-5

b7 = BIT SET TO INDICATE THAT A MISMATCH PLANE
ASSIGNMENT WAS DETECTED
b6, b5 = BITS ALLOCATED TO INDICATE WHICH PLANE WAS
SELECTED (A OR B)
b4 = BIT SET TO INDICATE THAT A "SYSTEM SLOT INSTALLED"
BIT WAS RECEIVED
b3 = BIT SET TO INDICATE THAT SFI PLANES ALIGNMENT
ERRORS ARE GENERATED
b2 = BIT SET TO INDICATE THAT THE SELECTED 8 KHz IS LOST
b1 = BIT SET TO INDICATE AN OR -ED FUNCTION OF ALL BCTM
(A) ALARMS
b0 = BIT ASSERTED BY CSTG TO INDICATE THAT A BIP-4 ERROR
WAS DETECTED



714-6

b3 TO b0 = BITS ALLOCATED FOR BIP-4

FIG. 20R

BCTM (B)
FIELD F7 (BYTE #1)

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

714-7

b7, b6 = BITS ALLOCATED FOR PSU #1 (A) ALARM
 b5, b4 = BITS ALLOCATED FOR PSU #2 (B) ALARM
 b3, b2 = BITS ALLOCATED FOR TWO FAN ALARMS
 b1, b0 = BITS ALLOCATED FOR ESWT PWR ALARM

FIELD F7 (BYTE #2)

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

714-7

b7 = BIT ALLOCATED FOR BREAKER PANEL ALARM
 b6 = BIT ALLOCATED FOR TEMP CONTROL
 b5 = BIT ALLOCATED TO INDICATE PLANE A LOST (LOC, LOS, SFI LOF)
 b4 = BIT ALLOCATED TO INDICATE PLANE B LOST (LOC, LOS, SFI LOF)
 b3 = BIT ALLOCATED TO INDICATE "TWO BITS VIOLATION" OR BIP-8
 ERRORS PLANE A (ONLY FOR THE FRAME ALLOCATED FOR THAT
 PARTICULAR RACK)
 b2 = BIT ALLOCATED TO INDICATE "TWO BITS VIOLATION" OR BIP-8
 ERRORS PLANE B (ONLY FOR THE FRAME ALLOCATED FOR THAT
 PARTICULAR RACK)
 b1 = ONE BIT TO INDICATE THAT A FORCED ALARM BIT WAS RECEIVED
 b0 = RESERVED

FIG. 20S

FIELD F8 (BYTE #1)

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

714-8

b7 TO b0 = HIGHER 8 BITS OF THE BCTM ID

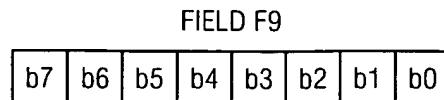
FIELD F8 (BYTE #2)

b7	b6	b5	b4	b3	b2	b1	b0
----	----	----	----	----	----	----	----

714-8

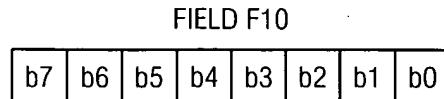
b7 TO b4 = LOWER 4 BITS OF THE BCTM ID
 b3 = BIT SET TO INDICATE THAT AN 8 KHz CLOCK WAS SELECTED
 b2 TO b0 = BITS ALLOCATED TO INDICATE WHICH CLOCK WAS
 SELECTED

FIG. 20T



b7 TO b0 = BITS ALLOCATED TO INDICATE PBA "HEALTH"
FROM SLOT #0 TO SLOT #7

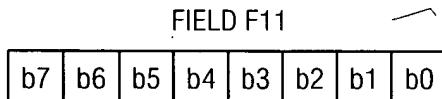
714-9



b7 TO b0 = BITS ALLOCATED TO INDICATE PBA "PRESENCE"
FROM SLOT #0 TO SLOT #7

714-10

FIG. 20U



714-11

b7 = BIT SET TO INDICATE THAT A PLANE ASSIGNMENT ERROR
WAS DETECTED

b6, b5 = BITS ALLOCATED TO INDICATE WHICH PLANE WAS
SELECTED (A OR B)

b4 = BIT SET TO INDICATE THAT A "SYSTEM SLOT INSTALLED"
BIT WAS RECEIVED

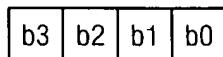
b3 = BIT SET TO INDICATE THAT SFI PLANES ALIGNMENT
ERRORS ARE GENERATED

b2 = BIT SET TO INDICATE THAT THE SELECTED 8 KHz IS LOST

b1 = BIT SET TO INDICATE AN OR -ED FUNCTION OF ALL BCTM
(B) ALARMS

b0 = BIT ASSERTED BY CSTG TO INDICATE THAT A BIP-4 ERROR
WAS DETECTED

FIELD F12



714-12

b3 TO b0 = BITS ALLOCATED FOR BIP-4